		REV	DATE	REVISION RECORD				AUTH
INPUT	MECHANICAL	-	09-14-09	Initial Release			VG	
	Dimensions	А	02-18-10	Revised input power level to +13 dBm			VG	JR
Frequency 10 MHz	$4.40 \times 4.00 \times 1^{\circ}$	В	03-10-10	Revised phase noise, harmonic spec,			VG	JR
Level	Connectors	С	01-16-12	mechanical tolerance, and power consumption Phase Noise, Spectral Purity; Drawing			PAC	
+13 dBm ±1 dB into 50 ohms			01 10 12	1 11030 110130, 0	peetran unty, D	awing	17.0	
	RF Input/Output: SMA(f)							
	Power, Monitoring: Feed Thru Terminals			J1PM	1 MXO Connectio	ons		
Frequency	GND: Ground Turret			Connector	Function			
1 GHz	Packaging			1	Supply Voltage			
Level	Nickel-plated machined			2 4	Ground, Case RF Output			
+13 dBm \pm 2 dB into 50 ohms	aluminum housing – J1PM			5	Phase Lock Vol	tage		
STABILITY	Mounting			6	Phase Lock Ala External Refere			
Aging (free-running)	Threaded inserts on base,			/		ince input		
1 x 10 ⁻⁶ first year	#2-56, 6 places							
after 30 days operating, typical	POWER REQUIREMENTS		1.00 -			<u>0-0</u>	_	
5 x 10 ⁻⁷ second year, typical	Warm-Up Power		0.75 — 7 🕲		0 10			
3×10^{-7} per year thereafter, typical	\leq 13 Watts for 5 minutes		0.44 —	5 6	2 ©	4		
	Total Power							
Phase Noise L(f), (free-running) 10 Hz -77 dBc/Hz	≤ 10 Watts at +25°C		0 —					
	Supply Voltage		0	0.55 1.33	2.40	3.85	40	
100 Hz -109 dBc/Hz	+15 VDC ±5%						4	
1 KHz -136 dBc/Hz	ADJUSTMENT		4.00 -		<u>h</u>			
10 KHz -153 dBc/Hz	Loop BW		3.915				$\langle 0 \rangle$	
100 KHz -154 dBc/Hz	Target Bandwidth: ~200 Hz							
Temperature Stability	Type 2 Loop							
±5 x 10 ⁻⁷ free-running from 0 to +50°C	CRYSTAL							
(Ref. +25°C)	Туре							
Phase Lock Alarm	100 MHz SC-cut (x10)							
TTL	OTHER							
Locked: +3.5 VDC to +5.2 VDC (Hi)	Label		2.000 — 🔿				0	
Out-of-Lock: +0.8 VDC max (Lo)	Use conventional label with the							
Phase Lock Voltage Monitor	following information:							
Voltage monitor pin supplied	501-21440 (Current Rev.)							
SPECTRAL PURTIY	1 GHz MXO-PLM							
Harmonics	+15 VDC	Threaded Inserts, #2⊂366, ∫ places, 0.190" desp						
≤-25 dBc	Serial # - Date Code			6 places, 0.190	" deep			
Sub-Harmonics	(Mark connectors with function)		0.085				<u>⁄o</u>	
≤-60 dBc	Test Data		- 5					
PLL Reference Products	Output Level		0).085				4.40	
≤-60 dBc	Phase Noise – free-running		5				4	
Spurious	Harmonics, Subs, Products, Spurious	Wenzel Associates, Inc.						
≤-80 dBc, excluding power	Power – Warm-up and Total	Austin, Texas						
supply line related spurs	Fower – wann-up and Total	Title:						
supply line related spuis		1 (GHz Mul	tiplied Cr	ystal Osc	illator (M	XO-Pl	_M)
		P/N: Rev: Date: Drawn: Ref:						
			501-21440 C 01-16-12		1-16-12		1.01.	
		Tolerances: (except as r		0.XX Dec:	0.XXX Dec:	FSCM:	Page 1	1
		Dimensions	are in inches	±0.030"	±0.015"	62821	Page 1 0	л і