		251						
		REV	DATE 12-14-10	REVISION RECORD			DWN PAC	AUTH
INPUT	Connectors	A 01-17-12 Phase Noise; Power; Input Level Range				PAC		
Frequency	RF Input/Output: SMA(f)		••••	1 1.000	, 1 0,per ,	90	1	
10 MHz	Power, Monitoring: Feed Thru Terminals							
Level	GND: Ground Turret		[					
$+7 \text{ dBm} \pm 6 \text{ dB}$ into 50 ohms	Packaging			100 1				
OUTPUT	Nickel-plated machined			J2P N	1XO Connections	_		
Frequency	aluminum housing – J2P			Connector	Function			
2 GHz	Mounting			1 2	Supply Voltage Ground, Case			
Level	Threaded inserts on base,			4	RF Output			
+13 dBm $\pm$ 2 dB into 50 ohms	#2-56, 6 places			5 6	Phase Lock Voltage Phase Lock Alarm			
STABILITY	POWER REQUIREMENTS			7	External Reference Input			
Aging (free-running)	Warm-Up Power					_		
1 x 10 <sup>-6</sup> first year	≤ 13 Watts for 5 minutes	1.	00 —					
after 30 days operating, typical	Total Power		75 — 🛈 🤅	0 10				
5 x 10 <sup>-7</sup> second year, typical	≤ 10 Watts at +25°C		44 - 5 6	2 ©		4		
$3 \times 10^{-7}$ per year thereafter, typical	Supply Voltage		25 - 700	<u>)</u>				
Phase Noise L(f), typical, (free-running)	+15 VDC ±5%		0	·				l
100 Hz -102 dBc/Hz			0.40 - 0.64 -	0.68 - 1.45 -		3.85 - 4.40 -		
1 KHz -129 dBc/Hz	Loop BW		o o			]] 3.85 4. <b>4</b> 0		l
10 KHz -146 dBc/Hz	Target Bandwidth: ≤ 10 Hz							l
100 KHz -147 dBc/Hz	Type 2 Loop	4.0 3.9	00 15 O			Q		l
Temperature Stability	CRYSTAL	0				Ì		l
$\pm 5 \times 10^{-7}$ free-running from 0 to $\pm 50^{\circ}$ C								l
(Ref. +25°C)	100 MHz SC-cut (x20)							l
Harmonics	OTHER							l
-25 dBc	Label							
Sub-Harmonics	Use conventional label with the following information:							
-60 dBc	following information:	2.0	00 - 0			0		
PLL Divider Products	501-23356 (Current Rev.)							
-60 dBc	2 GHz MXO-PLD +15 VDC							
Spurious	Serial # - Date Code							
-80 dBc, excluding power								
supply line related spurs	(Mark connectors with function) Test Data		_	Threaded Inserts, # 6 places, 0.190" de				
Phase Lock Alarm	- Output Level		85 -		-			
TTL	- Phase Noise – free-running	0.08	°° 1 <u>8</u>					
Locked: +3.5 VDC to +5.2 VDC (Hi)	- Temperature Stability – free-running		15			ر م م		
Out-of-Lock: +0.8 VDC max (Lo)	- Harmonics, Subs, Products, Spurious		0.085			4.315 4.40		
Phase Lock Voltage Monitor	- Power – Warm-up and Total							
Voltage monitor pin supplied	- Fower – Wann-up and Total	Wenzel Associates, Inc.						
MECHANICAL		Austin, Texas						
Dimensions		Title:						
4.4 x 4 x 1"		2 GHz Multiplied Crystal Oscillator (MXO-PLD)						
		P/N:	P/N: Rev: Date: Drawn: Ref:				f:	
		50	)1-23356	Α	01-17-12			ľ
		Tolerances		0.XX Dec:	0.XXX Dec: FS0	CM:		
		(except as Dimensions	noted) s are in inches	±0.030"	-	2821	Page <b>1</b> o	of <b>1</b>