		REV DATE REVISION RECORD			VISION RECORD	DWN	AUTH
INPUT	Connectors	-	03-28-12	Initial Release		PAC	
Frequency	RF Input/Output: SMA(f)					_	
10 MHz	Power, Monitoring: Feed Thru Terminals						
Level	GND: Ground Turret						
$+7 \text{ dBm } \pm 6 \text{ dB into } 50 \text{ ohms}$	Packaging						
OUTPUT	Nickel-plated machined	J3P MXO Connection			MXO Connections		
Frequency	aluminum housing – J3P	Connector Function					
5.12 GHz	Mounting			1	Supply Voltage		
Level	Threaded inserts on base,			2	Ground, Case		
+13 dBm ± 2 dB into 50 ohms	#2-56, 6 places			4	RF Output Phase Lock Voltage		
STABILITY	POWER REQUIREMENTS			6	Phase Lock Alarm		
Aging (free-running)	Warm-Up Power			7	External Reference Input		
1×10^{-6} first year	≤ 17 Watts for 5 minutes						
	Total Power	1.00	-				7
after 30 days operating, typical	≤ 13.5 Watts at +25°C	0.75		1 🕥		ţ,	
5 x 10 ⁻⁷ second year, typical	Supply Voltage	0.44	-	2 🔘		4	
3×10^{-7} per year thereafter, typical	+15 VDC ±5%	0.25	- 700	9			
Phase Noise L(f), typical, (free-running)		0					
100 Hz -93 dBc/Hz	Loop BW		0.40 0.64 0.68	1.45		4.81	5.36
1 KHz -118 dBc/Hz	Target Bandwidth: ≤ 10 Hz		• • •			4	с О
10 KHz -135 dBc/Hz	Type 2 Loop	4.00	ا ــــر طبر				
100 KHz -136 dBc/Hz	CRYSTAL	4.00 3.915	<u> </u>			4	2
Temperature Stability			e				
$\pm 5 \times 10^{-7}$ free-running from 0 to $\pm 50^{\circ}$ C	Type 80 MHz SC-cut (x64)						
(Ref. +25°C)	OTHER						
Harmonics	Label						
-25 dBc	Use conventional label with the						
Sub-Harmonics							
-60 dBc	following information: 501-25403 (Current Rev.)	2.000	-0)
PLL Divider Products	5.12 GHz MXO-PLD						
-60 dBc	5.12 GHZ MAO-PLD +15 VDC						
Spurious	Serial # - Date Code						
-80 dBc, excluding power	(Mark connectors with function)						
supply line related spurs	· · · · · · · · · · · · · · · · · · ·			hreaded Inserts, #2-56, places, 0.190" deep			
Phase Lock Alarm	Test Data			,,,			
TTL	- Output Level	0.085				/c	Ś
Locked: +3.5 VDC to +5.2 VDC (Hi)	- Phase Noise – free-running	U	15			لے	
Out-of-Lock: +0.8 VDC max (Lo)	- Temperature Stability – free-running		0.085			5.270	5.36
Phase Lock Voltage Monitor	- Harmonics, Subs, Products, Spurious		8			2,	
Voltage monitor pin supplied	- Power – Warm-up and Total			Nenzel A	ssociates, Ind	•	
MECHANICAL		Austin, Texas					
Dimensions		Title:					
5.36 x 4 x 1"		5.12	2 GHz M	ultiplied C	rystal Oscillato	' (MXO-I	PLD)
		P/N:		- Rev: Da	ate: Drawn:	Re	-
		50)1-25403	-	03-28-12		
		Tolerances		0.XX Dec:	0.XXX Dec: FSCM:		
		(except as		±0.030"	±0.010" 62821	Page 1 c	of 1
		Dimensions		10.030	±0.010 0202	9 0	