		REV DATE REVISION RECORD					DWN	AUTH
INPUT	MECHANICAL	-	01-27-14	Initial Release		PAC		
Frequency	Dimensions							
10 MHz	$4.40 \times 4 \times 1^{\circ}$							
Level	Connectors							
\pm 13 dBm \pm 1 dB into 50 ohms	RF Input/Output: SMA(f)							
OUTPUTS	Power, Monitoring: Feed Thru Terminals			J1PM-01 M	XO Connections			
	GND: Ground Turret			Connector F	unction			
RF Output Frequency (into 50 chmc)	Packaging			1 9	iupply Voltage			
(into 50 ohms)	Nickel-plated machined			4 F	Fround, Case			
A 100 MHz +13 dBm ±2 dB	aluminum housing – J1PM-01			6 F	hase Lock Voltage hase Lock Alarm ixternal Reference Input			
B 500 MHz +13 dBm ±2 dB	Mounting				F Output A			
STABILITY	Threaded inserts on base,	1	.00 —					
Aging (free-running)	6 places, #2-56		.75 - 7000) O O 5 6	10	2		
1×10^{-6} first year	POWER REQUIREMENTS	0	.44 —	56	2 @	4		
after 30 days operating, typical	Warm-Up Power		0-					
	≤ 13.5 Watts for 5 minutes							
5×10^{-7} second year, typical	Total Power		0.55	1.33	2.40	3.85		
3×10^{-7} per year thereafter, typical	≤ 10 Watts at +25°C			адд	A 4		IE	
Phase Noise L(f), typical, (free-running)	Supply Voltage	4	.00 915 - C			~ ~		
100 MHz 500 MHz	+15 VDC ±5%							
100 Hz -130 -115	ADJUSTMENT						•	
1 kHz -158 -143	Loop BW							
10 kHz -175 -159	Target Bandwidth: ~200 Hz							
100 kHz -176 -160	Type 2 Loop							
	CRYSTAL	2.0	000 — 🖸			Q	5	
Temperature Stability	Туре							
$\pm 5 \times 10^{-7}$ free-running from 0 to $\pm 50^{\circ}$ C	100 MHz SC-cut (x5)							
	OTHER							
(Ref. +25°C) Harmonics	Label	Threaded Inserts, #2-56, 6 places, 0.190° deep						
-25 dBc	Use conventional label with the	0.0	185 7			2	Ц	
Sub-Harmonics	following information:		0-15			<u>_</u>	Ę	╤╉
-60 dBc	501-25486 (Current Rev.)		0.085		_	4.315		
PLL Reference Products	100M/500M MXO-PLM		0		-2.47	4		
-60 dBc	+15 VDC		0					
Spurious	Serial # - Date Code				8			
-80 dBc, excluding power	(Mark connectors with function)	0	.75 —		Č.			
supply line related spurs	Test Data							
Phase Lock Alarm	- Output Level	Wenzel Associates, Inc.						
TTL	- Phase Noise – free-running							
Locked: +3.5 VDC to +5.2 VDC (Hi)	- Harmonics, Subs, Products, Spurious	Title:						
Out-of-Lock: +0.8 VDC max (Lo)	- Power – Warm-up and Total	100 MHz & 500 MHz						
Phase Lock Voltage Monitor		Multiplied Crystal Oscillator (MXO-PLM)						
Electrical tuning monitor pin supplied		P/N: Rev: Date: Drawn: Ref:						
		50 ⁻	1-25486	-	01-27-14			
		Tolerances		0.XX Dec:	0.XXX Dec:	FSCM:		
		(except as Dimensions	noted) s are in inches	±0.030	' ±0.010"	62821	Page 1 c	of 1
					1			