		REV	DATE	REVISION RECORD	DWN	AUTH
INPUT	Phase Lock Voltage Monitor	-	03-26-12	Initial Release	PAC	
Frequency	Voltage monitor pin supplied					
10 MHz	MECHANICAL					
Level	Dimensions					
$+7 \text{ dBm} \pm 6 \text{ dB}$ into 50 ohms	4.4 x 4 x 1"					
	Connectors			J2P-03 MXO Connections		
	RF Input/Outputs: SMA(f)			Connector Function		
Frequency 100 MHz	Power, Monitoring: Feed Thru Terminals			1 Supply Voltage		
	GND: Ground Turret			2 Ground, Case 4 RF Output B		
				5 Phase Lock Voltage 6 Phase Lock Alarm		
+13 dBm ±2 dB into 50 ohms OUTPUT B	Packaging Nickel-plated machined			7 External Reference Input 8 RF Output A		
	aluminum housing – J2P-03					
Frequency	Mounting		1.00 —			
1 GHz	Threaded inserts on base,		0.75 — 🗿 🕻			
			0.44 — 0.25 — 7 ©	20		
+13 dBm ±2 dB into 50 ohms	#2-56, 6 places POWER REQUIREMENTS		0			
STABILITY	Warm-Up Power		0 - 0 0.40	0.68 U 1.45 L 4.40 L 1.45 L 1.45 L		
Aging (free-running)	≤ 14 Watts for 5 minutes		àď			
1 x 10 ⁻⁶ first year	Total Power		4.00 -			
after 30 days operating, typical	≤ 10 Watts at +25°C		3.915 - 9	Q		
5 x 10 ⁻⁷ second year, typical	Supply Voltage					
3 x 10 ⁻⁷ per year thereafter, typical	+15 VDC ±5%					
Phase Noise L(f), dBc/Hz, typical,						
(free-running)	Loop BW					
<u>100 MHz 1 GHz</u>	Target Bandwidth: ≤ 10 Hz					
100 Hz -130 -109	Type 2 Loop		2.000 — 🔿	0		
1 kHz -158 -136	CRYSTAL					
10 kHz -174 -152						
100 kHz -175 -154	Type 100 MHz SC-cut (x10 and filter)					
1 MHz -176 -165	OTHER			Threaded Inserts, #2-56, 6 places, 0.190" deep		
10 MHz -176 -165	Label					
Temperature Stability	Use conventional label with the		0.085 -	Ó		
$\pm 5 \times 10^{-7}$ free-running from 0 to $\pm 50^{\circ}$ C	following information:		080			
(Ref. +25°C)	501-25711 (Current Rev.)		0.00	.51 5 4.315 4.315		
Harmonics	100M/1GHz MXO-PLD		0	Ţ		
-25 dBc	+15 VDC					
Sub-Harmonics	Serial # - Date Code			8		
-60 dBc	(Mark connectors with function)		0.75 —			
PLL Divider Products	Test Data					
-60 dBc	- Output Level			Nenzel Associates, Inc.		
Spurious	- Phase Noise – free-running			Austin, Texas		
-80 dBc, excluding power	- Temperature Stability – free-running	Title:				
supply line related spurs	- Harmonics, Subs, Products, Spurious	100	MHz & 1	GHz Multiplied Crystal Oscillator	(MXO-I	PLD)
Phase Lock Alarm	- Power – Warm-up and Total	P/N:		Rev: Date: Drawn:	Re	ef:
TTL			1-25711	- 03-26-12		
Locked: +3.5 VDC to +5.2 VDC (Hi)		Tolerances				
Out-of-Lock: +0.8 VDC max (Lo)		(except as		0.XX Dec: 0.XXX Dec: FSCM: ±0.030" ±0.010" 62821	Page 1 c	of 1
			are in inches			