INPUT	MECHANICAL		
Frequency 10 MHz	Dimensions 4.4 x 4 x 1"		
Level	Connectors		
+7 dBm ±6 dB into 50 ohms	RF Input/Output: SMA(f)		
OUTPUT	Power, Monitoring: Feed Thru Terminals		
Frequency	GND: Ground Turret		
1 GHz	Packaging		
Level	Nickel-plated machined		
+13 dBm ±2 dB into 50 ohms	aluminum housing – J2P		
STABILITY	Mounting		
Aging (free-running)	Threaded inserts on base,		
1 x 10 <sup>-6</sup> first year	#2-56, 6 places		
after 30 days operating, typical	POWER REQUIREMENTS		
5 x 10 <sup>-7</sup> second year, typical	Warm-Up Power		
3 x 10 <sup>-7</sup> per year thereafter, typical	≤ 14 Watts for 5 minutes		
	Total Power		
Phase Noise L(f), (free-running)	≤ 10.5 Watts at +25°C		
100 Hz -109 dBc/Hz 1 KHz -136 dBc/Hz	Supply Voltage		
	+15 VDC ±5%		
10 KHz -153 dBc/Hz	ADJUSTMENT		
100 KHz -154 dBc/Hz 1 MHz -160 dBc/Hz	Loop BW		
10 MHz -165 dBc/Hz	Target Bandwidth: ≤ 10 Hz		
Temperature Stability	Type 2 Loop		
±5 x 10 <sup>-7</sup> free-running from 0 to +50°C	CRYSTAL		
(Ref. +25°C)	Type		
Harmonics	100 MHz SC-cut (x10) OTHER		
-25 dBc	Design		
Sub-Harmonics	Includes a SAW filter for improving the		
-60 dBc	phase noise beyond the ~300 kHz offset.		
PLL Divider Products	Label		
-60 dBc	Use conventional label with the		
Spurious	following information:		
-80 dBc, excluding power	501-25722 (Current Rev.)		
supply line related spurs	1 GHz MXO-PLD		
Phase Lock Alarm	+15 VDC		
TTL	Serial # - Date Code		
Locked: +3.5 VDC to +5.2 VDC (Hi)	(Mark connectors with function)		
Out-of-Lock: +0.8 VDC max (Lo)	Test Data		
Phase Lock Voltage Monitor	- Output Level		
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- Phase Noise - free-running

- Power - Warm-up and Total

- Temperature Stability - free-running

- Harmonics, Subs, Products, Spurious

Voltage monitor pin supplied

REV	DATE	REVISION RECORD	DWN	AUTH
-	06-04-12	Initial Release	PAC	

J2P MXO Connections		
Connector	Function	
1 2 4 5	Supply Voltage Ground, Case RF Output Phase Lock Voltage Phase Lock Alarm External Reference Input	



