		REV DATE - 06-04-12 In		RE	REVISION RECORD		AUTH	
INPUT	MECHANICAL			Initial Release		PAC		
	Dimensions							
Frequency 10 MHz	5.36 x 4 x 1"							
Level	Connectors							
+7 dBm ±6 dB into 50 ohms	RF Input/Output: SMA(f)							
	Power, Monitoring: Feed Thru Terminals			12PI	M MXO Connections]		
	GND: Ground Turret			Connector	Function			
Frequency				1	Supply Voltage			
1 GHz Level	Packaging			2	Ground, Case			
	Nickel-plated machined			4	RF Output Phase Lock Voltage			
+13 dBm ±2 dB into 50 ohms	aluminum housing – J2PM Mounting			6	Phase Lock Alarm			
STABILITY				7	External Reference Input			
Aging (free-running)	Threaded inserts on base,							
1 x 10 ⁻⁶ first year	#2-56, 6 places	1.0	00 —				=	
after 30 days operating, typical	POWER REQUIREMENTS	0.7	75 — 7 💽 🔿	စ်စိုစို	1 🖸			
5 x 10 ⁻⁷ second year, typical	Warm-Up Power	0.4	н —	56	2 🛇	4		
3×10^{-7} per year thereafter, typical	≤ 16 Watts for 5 minutes Total Power	0.2				F		
Phase Noise L(f), (free-running)			0 -					
100 Hz -109 dBc/Hz	≤ 13 Watts at +25°C		.0 .53	1.33	2.40	4.81	5.36	
1 KHz -136 dBc/Hz	Supply Voltage		•		5	4	5	
10 KHz -153 dBc/Hz	+15 VDC ±5% ADJUSTMENT	4.0		<u>а фф</u>	fh	<u> </u>		
100 KHz -154 dBc/Hz		3.91	.5 5 9				୧	
1 MHz -160 dBc/Hz	Loop BW							
10 MHz -165 dBc/Hz	Target Bandwidth: ~200 Hz							
Temperature Stability	Type 2 Loop CRYSTAL							
$\pm 5 \times 10^{-7}$ free-running from 0 to $\pm 50^{\circ}$ C								
(Ref. +25°C)	Type 100 MHz SC-cut (x10)							
Harmonics	OTHER	-				(
-25 dBc	Design	2.00				l	0	
Sub-Harmonics	Includes a SAW filter for improving the							
-60 dBc	phase noise beyond the ~300 kHz offset.							
PLL Reference Products	Label							
-60 dBc	Use conventional label with the			Threaded Inserts,				
Spurious	following information:			#2-56, 6 places, 0.190" deep				
-80 dBc, excluding power	501-25759 (Current Rev.)	0.08	15 - 1				0	
supply line related spurs	1 GHz MXO-PLM		0 - 5			· · · · ·		
Phase Lock Alarm	+15 VDC		085				5.36	
TTL	Serial # - Date Code		0.0			Ľ	с <u>го</u>	
Locked: +3.5 VDC to +5.2 VDC (Hi)	(Mark connectors with function)			A/		-		
Out-of-Lock: +0.8 VDC max (Lo)	Test Data	Wenzel Associates, Inc.						
Phase Lock Voltage Monitor	- Output Level	Austin, Texas Title:						
Voltage monitor pin supplied	- Phase Noise – free-running	1 GHz Multiplied Crystal Oscillator (MXO-PLM)						
	 Temperature Stability – free-running 							
	- Harmonics, Subs, Products, Spurious	P/N:	1_25750	Rev: Date:	6-04-12	Ref: 2	3202a	
	 Power – Warm-up and Total 	50	1-25759			_		
		Tolerances (except as	noted)	0.XX Dec:	0.XXX Dec: FSCM:	1 1		
			s are in inches	±0.030"	±0.010" 6282	1 Page 1	DT I	