Α	10 MHz	+13 :	±2 dBm		
В	100 MHz	+13 :	±2 dBm		
С	1 GHz	+13 :	±2 dBm		
C STABILIT Aging 1 x 10 after 3 5 x 10 3 x 10 Phase No 10 Hz 100	1 GHz TY Tr Tr first year 0 days operates second year second year per year the bise L(f), dBo 10 MHz -138 -162 -167 -172 -174 -175 ture Stability 0 of 9, 0 to +50 of cs dBc monics dBc rence Products	+13 : ting, typical r, typical ereafter, typ t/Hz, typica 100 MHz -116 -138 -137 -154 -175 -176 C (Ref. +25	ical I 1 GHz -94 -116 -115 -132 -153 -154		
≤ -80 dBc, excluding power supply line related spurs					
Phase Lock Alarm TTL					
Locked: +3.5 VDC to +5.2 VDC (Hi) Out-of-Lock: +0.8 VDC max (Lo) Phase Lock Voltage Monitor Voltage monitor pin supplied MECHANICAL Dimensions 6.51 x 4 x 1"					
Connectors RF Outputs: SMA(f) Power, Monitoring: Feed Thru Terminals GND: Ground Turret					

Packaging

Mounting

Level (into 50Ω)

Nickel-plated machined

Threaded inserts on base,

≤ 19.5 Watts for 5 minutes

Mechanical Tuning (Internal 10 MHz)

Loop BW (Internal 100 MHz PLL)

Target Bandwidth: ~400 Hz

Use conventional label with the

(Mark connectors with function)

- Power - Warm-up and Total

100 MHz SC-cut (x10)

following information: 501-25913 (Current Rev.) 10M/100M/1GHz MXO-PLMX

Serial # - Date Code

- Temperature Stability

≤ 13 Watts at +25°C

#2-56, 11 places **POWER REQUIREMENTS**

Warm-Up Power

Supply Voltage

ADJUSTMENT

 $\pm 1 \times 10^{-6}$

CRYSTAL

Type

OTHER Label

Type 2 Loop

+15 VDC

- Output Level - Phase Noise

Test Data

+15 VDC ±5%

Total Power

aluminum housing – J2PMX-03

OUTPUTS

Output Frequency

REV	DATE	REVISION RECORD	DWN	AUTH
-	05-17-12	Initial Release	PAC	

J2PMX-03 MXO Connections		
Connector	Function	
1 2 4 5 6 8	Supply Voltage Ground, Case RF Output C Phase Lock Voltage Phase Lock Alarm RF Output A	
9	RF Output B	





