INPUT	
Frequency	
10 MHz, ±5 x 10 ⁻⁷	
Level	
+7 dBm ±5 dB into 50 ohms	
OUTPUT	
Frequency	
100 MHz	
Level	
+13 dBm ±2 dB into 50 ohms	
STABILITY	
Aging -6	
1 x 10 ⁻⁶ first year	
after 30 days operating, typical	
5 x 10 ⁻⁷ second year, typical	
3 x 10 ⁻⁷ per year thereafter, typical	
Phase Noise L(f), static, free-running	
100 Hz -130 dBc/Hz	
1 kHz -150 dBc/Hz	
10 kHz -169 dBc/Hz	
100 kHz -170 dBc/Hz	
Temperature Stability	
±5 x 10 ⁻⁷ free-running from 0 to +50°	C.
(Ref. +25°C)	-,
Harmonics	
-30 dBc	
Sub-Harmonics	
-80 dBc	
PLL Divider Products	
-80 dBc	
Non-Harmonic Spurious	
-80 dBc, excluding power	
supply line related spurs	
Phase Lock Alarm	
TTL	
Locked: +3.5 VDC to +5.2 VDC (Hi)	
Out-of-Lock: +0.8 VDC max (Lo)	
Phase Lock Voltage Monitor	
Voltage monitor pin supplied MECHANICAL	
Dimensions 2 x 2 x 1.3"	
Connectors	
SMA(f) and solder pins on side	
Civin(i) and solder pins on side	

Packaging

Warm-Up Power

Supply Voltage +15 VDC ±5% ADJUSTMENT Loop BW

Type 2 Loop

ENVIRONMENTAL

Operating Temperature 0° to +50°C

following information:

100 MHz PL Citrine

Serial # - Date Code

501-26228 (Current Rev.)

Storage Temperature

-40° to +85°C

+15 VDC

Output Level

Test Data

OTHER

Label

CRYSTAL Type

Total Power

Nickel-plated machined aluminum case - CVPLO POWER REQUIREMENTS

≤ 8 Watts for 5 minutes

Target Bandwidth: ≤ 10 Hz

100 MHz SC-cut (low-g)
Acceleration Sensitivity

 \leq 3 x 10⁻¹⁰/g per axis, typical

Use conventional label with the

Phase Noise, Static, Free-Running

Power – Warm-up and Total

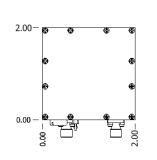
Acceleration Sensitivity

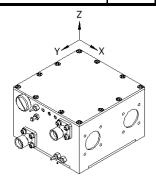
Temperature Stability, Free-Running

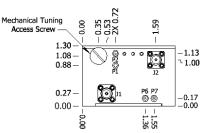
Harmonics, Subs, Products, Spurious

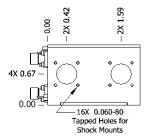
≤ 5 Watts at +25°C

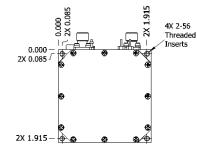
REV	DATE	REVISION RECORD	DWN	AUTH
-	08-30-12	Initial Release	PAC	JR











	CVPLO
Connector	Function
P1	Ground, Case
P2	Supply Voltage
P6	Phase Lock Voltage Monitor
P7	Phase Lock Alarm
J1	External Reference Input
J2	RF Output

Connector numbers are for reference only and will not be marked on unit.

M

Title:

Wenzel Associates, Inc.

Austin, Texas

Standard 100 MHz-SC Phase Lock
Citrine Crystal Oscillator

Rev: Date: Drawn:

P/N:	Rev:	Date):	Drawn:		Ref:
501-26228	-	0	8-30-12			SPR
Tolerances: (except as noted) Dimensions are in inches	0.XX Dec: ±0.030'		0.XXX Dec: ±0.010"	FSCM: 62821	P	Page 1 of 1