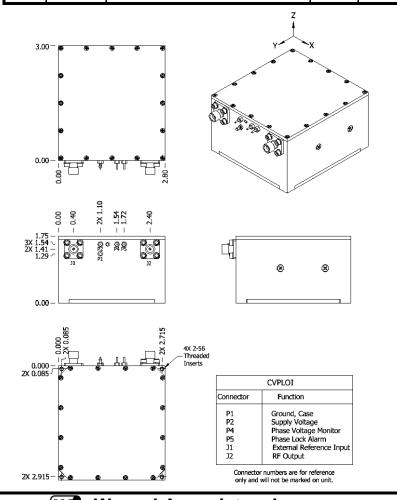
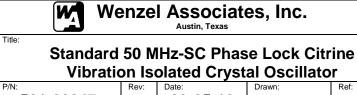
INPUT Frequency 10 MHz, ±5 x 10 ⁻⁷ Level +7 dBm ±5 dB into 50 ohms OUTPUT Frequency 50 MHz Level +13 dBm ±2 dB into 50 ohms STABILITY Aging 1 x 10 ⁻⁶ first year after 30 days operating, typical 5 x 10 ⁻⁷ second year, typical 3 x 10 ⁻⁷ per year thereafter, typical Phase Noise L(f), static, free-running 100 Hz -135 dBc/Hz 1 kHz -155 dBc/Hz 10 kHz -156 dBc/Hz 100 kHz -170 dBc/Hz Temperature Stability ±5 x 10 ⁻⁷ free-running from 0 to +50°C, (Ref. +25°C) Harmonics -30 dBc Sub-Harmonic Spurious -80 dBc Non-Harmonic Spurious -80 dBc, excluding power supply line related spurs Phase Lock Alarm TTL Locked: +3.5 VDC to +5.2 VDC (Hi) Out-of-Lock: +0.8 VDC max (Lo) Phase Lock Voltage Monitor Voltage monitor pin supplied MECHANICAL Dimensions 2.8" x 3.0" x 1.75" Connectors SMA(f) and solder pins on side	Packaging Nickel-plated machined aluminum case – CVPLOI POWER REQUIREMENTS Warm-Up Power ≤ 8 Watts for 5 minutes Total Power ≤ 5 Watts at +25°C Supply Voltage +15 VDC ±5% ADJUSTMENT Loop BW Target Bandwidth: ≤ 10 Hz Type 2 Loop CRYSTAL Type 50 MHz SC-cut (low-g) Acceleration Sensitivity ≤ 5 x 10 ⁻¹⁰ /g per axis, typical ENVIRONMENTAL Operating Temperature 0° to +50°C Storage Temperature -40° to +85°C Resonance (Internal Mount Natural Frequency) ~30 Hz, typical OTHER Label Use conventional label with the following information: 501-26247 (Current Rev.) 50 MHz PL Citrine +15 VDC Serial # - Date Code Test Data Output Level Phase Noise, Static, Free-Running Temperature Stability, Free-Running
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EV	DATE	REVISION RECORD	DWN	AUTH
-	09-05-12	Initial Release	PAC	JR





501-26247	Rev:	Oate	9-05-12	Drawn:		Ref: SPR
Tolerances: (except as noted) Dimensions are in inches	0.XX Dec: ±0.03		0.XXX Dec: ±0.010"	FSCM: 62821	Pa	age 1 of 1