INPUT
Frequency
10 MHz
Level
+7 dBm ±6 dB into 50 ohms
OUTPUT
Frequency
12 GHz
Level
+13 dBm ±2 dB into 50 ohms
STABILITY
Aging (free-running)
1 x 10 <sup>-6</sup> first year
after 30 days operating, typical
5 x 10 <sup>-7</sup> second year, typical
3 x 10 <sup>-7</sup> per year thereafter, typical
Phase Noise L(f), typical, (free-running)
100 Hz -83 dBc/Hz
1 KHz -109 dBc/Hz
10 KHz -127 dBc/Hz
100 KHz -132 dBc/Hz
Temperature Stability
$\pm 5 \times 10^{-7}$ free-running from 0 to $\pm 50^{\circ}$ C
(Ref. +25°C)
Harmonics
≤ -25 dBc
Sub-Harmonics
≤ -60 dBc
PLL Divider Products
≤ -60 dBc
Spurious
≤ -80 dBc, excluding power
supply line related spurs
Phase Lock Alarm
TTL
Locked: +3.5 VDC to +5.2 VDC (Hi)
Out-of-Lock: +0.8 VDC max (Lo)
Phase Lock Voltage Monitor Voltage monitor pin supplied
MECHANICAL
Dimensions
5.36 x 4 x 1"
Connectors
RF Input/Output: SMA(f)
Power, Monitoring: Feed Thru Terminals
GND: Ground Turret

#2-56, 6 places **POWER REQUIREMENTS** 

≤ 17 Watts for 5 minutes

Target Bandwidth: < 10 Hz

120 MHz SC-cut (x100)

following information: 501-26905 (Current Rev.) 12 GHz MXO-PLD

Serial # - Date Code

Use conventional label with the

(Mark connectors with function)

Temperature Stability - free-running

Phase Noise - free-running

Harmonics, Subs, Spurious Power – Warm-up and Total

≤ 13.5 Watts at +25°C

Warm-Up Power

**Supply Voltage** +15 VDC ±5% **ADJUSTMENT** 

Type 2 Loop

+15 VDC

Output Level

**Test Data** 

**Total Power** 

Loop BW

**CRYSTAL** 

**Type** 

**OTHER** 

Label

	REV	DATE	REVISION RECORD	DWN	AUTH
Packaging	-	06-09-13	Initial Release	Liz	
Nickel-plated machined					
aluminum housing – J3P					
Mounting					
Threaded inserts on base,					

J3P MXO Connections				
Connector	Function			
1 2 4 5 6 7	Supply Voltage Ground, Case RF Output Phase Lock Voltage Phase Lock Alarm External Reference Input			



