INPUT **Frequency Reference Input** 10 MHz Level +7 dBm ±6 dB into 50 ohms OUTPUTS

RF Output	Frequency	Output Level (into 50 ohms)
А	500 MHz	+13 dBm ±2 dB
В	1 GHz	+13 dBm ±2 dB
С	2.5 GHz	+13 dBm ±2 dB
RF Input	Frequency	Output Level (into 50 ohms)
Α	500 MHz	+13 dBm ±2 dB

## STABILITY

Aging (free-running) 1 x 10<sup>-6</sup> first year after 30 days operating, typical  $5 \times 10^{-7}$  second year, typical  $3 \times 10^{-7}$  per year thereafter, typical Phase Noise L(f), dBc/Hz, typical, (free-running)

(nee-running)				
	500 MHz	1 GHz	2.5 GHz	
10 Hz	-85	-79	-70	
100 Hz	-115	-109	-100	
1 kHz	-143	-136	-128	
10 kHz	-159	-153	-144	
100 kHz	-160	-154	-145	
1 MHz	-160	-154	-145	
10 MHz	-160	-154	-145	

## **Temperature Stability**

±5 x 10<sup>-7</sup> free-running from 0 to +50 °C (Ref. +25°C) Harmonics, Sub-Harmonics -25 dBc, -60 dBc PLL Divider Products -60 dBc Spurious -80 dBc, excluding power supply line related spurs

## Phase Lock Alarm Initial Release LR/PAC . 07-16-14 TTI Locked: +3.5 VDC to +5.2 VDC (Hi) Out-of-Lock: +0.8 VDC max (Lo) Phase Lock Voltage Monitor Voltage monitor pin supplied **MECHANICAL** J3P-13XX MXO Connections Dimensions Connector Function PRELIMINARY 5.36" x 4 x 1" Supply Voltage Ground, Case 2 Connectors RF Output C 4 Phase Lock Voltage RF Input/Outputs: SMA(f) 5 6 Phase Lock Alarm Power, Monitoring: Feed Thru Terminals 7 External Reference Input 89 **RF Output A** GND: Ground Turret **RF Output B** 10 RF Input A Packaging 1.00 Nickel-plated machined 3. ĵ. 0 0 5 6 10 0.75 aluminum housing - J2P-13XX 9 2 🔘 0.44 Mountina 5 70(0)0 0.25 Threaded inserts on base, 0 #2-56, 6 places 4.81 5.36 1.45 0 0.40 2.90 3.85 POWER REQUIREMENTS r Warm-Up Power 4.00 $\leq$ 13 Watts for 5 minutes 3.915 -**Total Power** ≤ 9 Watts at +25 °C Supply Voltage +15 VDC ±5% ADJUSTMENT Loop BW Target Bandwidth: ≤ 10 Hz 2.000 - 0 0 Type 2 Loop CRYSTAL Type 100 MHz SC-cut (x5, x2, 500 MHz x 5) Threaded Inserts, #2-56, OTHER places, 0,190" deep Label 0.085 Use conventional label with the 0 following information: 501-28265 (Current Rev.) 500M/1G/2.5G MXO-PLD Wenzel Associates, Inc. +15 VDC Serial # - Date Code Austin, Texas Title: (Mark connectors with function) 500 MHz, 1 GHz & 2.5 GHz Test Data Multiplied Crystal Oscillator (MXO-PLD) - Output Level P/N: Date: Drawn: Ref: Rev: - Phase Noise - free-running 501-28265 07-16-14 -- Temperature Stability - free-running - Harmonics, Subs, Products, Spurious Tolerances: 0.XX Dec: 0.XXX Dec: FSCM: (except as noted) Page 1 of 1 62821 - Power - Warm-up and Total ±0.030" ±0.010" Dimensions are in inches

REV

DATE

REVISION RECORD

DWN

AUTH