INPUT				Phase Lock Alarm TTL	REV	DATE			SION RECORD		DWN	AU
Frequenc	y Refere	ence Inpu	ıt		-	07-16-14	Initial Re	elease			LR/PAC	—
10 MH	-	-		Locked: +3.5 VDC to +5.2 VDC (Hi)								+
Level				Out-of-Lock: +0.8 VDC max (Lo)								+
		nto 50 oh	ms	Phase Lock Voltage Monitor								-
OUTPUTS	S			Voltage monitor pin supplied								
	out Era	auonov	Output Level (into 50 ohms)	MECHANICAL				G3P-13XX MXO Connections				
RF Outp	JUL FIE	Frequency		Dimensions PR	ELIMINA	RY	[Connector	Function			
А	50	00 MHz	+13 dBm ±2 dB	6.46" x 4 x 1" Connectors				1 2	Supply Voltage Ground, Case			
В		1 GHz	+13 dBm ±2 dB	RF Input/Outputs: SMA(f)				4 5	RF Output C Phase Lock Volta	ige		
		-		Power, Monitoring: Feed Thru Terminal	le			6 7	Phase Lock Alarn External Referen			
С	2.	.5 GHz	+13 dBm ±2 dB	GND: Ground Turret				8 9	RF Output A RF Output B			
RF Inp	ut Fre	equency	Output Level	Packaging			l	10	RF Input A			
-			(into 50 ohms)	Nickel-plated machined	1.00 -	5 🖸 🕥 6			0-4	0		0-0
A		00 MHz	+13 dBm ±2 dB	aluminum housing – G2P-13XX	0.75 —			00		5 8	9	
		th loop-		Mounting	0.50 -	7		0 0		6	9	10
	ba	ck cable		Threaded inserts on base,	0.25 —					6	0	
				#2-56, 6 places	Ϋ́́Ι							1
STABILIT				POWER REQUIREMENTS	0	_			3.90	20 1	¥.	5.91
Aging (fre	Aging (free-running)			Warm-Up Power				A.				
1 x 10 ⁻⁶ first year				\leq 13 Watts for 5 minutes	4.00 3.915 J			n		~ (-11	
after 30 days operating, typical				Total Power								
5 x 10^{-7} second year, typical				≤ 9 Watts at +25°C								
3×10^{-7} per year thereafter, typical				Supply Voltage								
Phase No	oise L(f),	dBc/Hz, t	typical,	+15 VDC ±5%								
(free-runr	ning)			ADJUSTMENT								
	500	1 GHz	2.5	Loop BW	2.000 — C	r						
	MHz		GHz	Target Bandwidth: ≤ 10 Hz	2.000							
10 Hz	-89	-83	-74	Type 2 Loop CRYSTAL								
100 Hz	-121	-115	-106									
1 kHz	-147	-140	-132	Type 100 MHz SC-cut (x5, x2, 500 MHz x 5)								
10 kHz	-167	-160	-152	OTHER			nserts, #2-56,					
100 kHz	-169	-162	-154	Label		6 places, 0.	.190" deep					
1 MHz	-170	-162	-155	Use conventional label with the	0.085 -	\						4
10 MHz	-170	-162	-155	following information:	الے '						88	Ϋ́́
To market and 4	Luna Otal			501-28266 (Current Rev.)	0.085							Ē
•	Temperature Stability ±5 x 10 ⁻⁷ free-running from 0 to +50°C			500M/1G/2.5G GMXO-PLD								
		unning fro	m U to +50°C	+15 VDC			Wen7	elΔs	sociate	es. Ind		
(Ref. +	(Ref. +25°C) Harmonics, Sub-Harmonics			Serial # - Date Code					stin, Texas	, iii		
	-25 dBc, -60 dBc			(Mark connectors with function)	Title:			_			_	
Harmonic				Test Data					z & 2.5 G			
Harmonic -25 dB						Multin	lied C	rvstal	Oscillato	r (GMX	(ח וס-ס)	,
Harmonic -25 dB PLL Divic	der Produ	ucts		- Output Level		multip			obolilato			
Harmonic -25 dB PLL Divic -60 dB	der Produ	ucts		- Phase Noise – free-running	P/N:		Re	v: Date	:	Drawn:		Ref:
Harmonic -25 dB PLL Divic -60 dB Spurious	der Produ		r			01-28266	Re	v: Date	7-16-14			
Harmonic -25 dB PLL Divic -60 dB Spurious -80 dB	der Produ c c, exclud	ing power		- Phase Noise – free-running	5	01-28266	Re	• Date • 0	:			Ref:

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