INPUT Frequency 10 MHz Level +7 dBm ±6 dB into 50 ohms OUTPUTS

RF Output	Frequency	Output Level (into 50 ohms)
Α	1 GHz	+13 dBm ±2 dB

+13 dBm ±2 dB

2 GHz

B **STABILITY**

Aging (free-running)

1 x 10⁻⁶ first year after 30 days operating, typical 5 x 10⁻⁷ second year, typical 3 x 10⁻⁷ per year thereafter, typical

Phase Noise L(f), dBc/Hz, typical, (free-running)

	1 GHz	2 GHz
100 Hz	-109	-102
1 kHz	-136	-129
10 kHz	-153	-146
100 kHz	-154	-147

Temperature Stability

 $\pm 5 \times 10^{-7}$ free-running from 0 to $+50^{\circ}$ C (Ref. $+25^{\circ}$ C)

Harmonics

-25 dBc

Sub-Harmonics

-60 dBc

PLL Divider Products

-60 dBc

Spurious

-80 dBc, excluding power supply line related spurs

Phase Lock Alarm

TTL

Locked: +3.5 VDC to +5.2 VDC (Hi) Out-of-Lock: +0.8 VDC max (Lo)

Phase Lock Voltage Monitor

Voltage monitor pin supplied

MECHANICAL

Dimensions

4.4 x 4 x 1"

Connectors

RF Input/Output: SMA(f)

Power, Monitoring: Feed Thru Terminals

GND: Ground Turret

Packaging

Nickel-plated machined aluminum housing – J2P-023

Mounting

Threaded inserts on base,

#2-56, 6 places

POWER REQUIREMENTS

Warm-Up Power

≤ 13 Watts for 5 minutes

Total Power

≤ 10 Watts at +25°C

Supply Voltage

+15 VDC ±5%

ADJUSTMENT

Loop BW

Target Bandwidth: ≤ 10 Hz

Type 2 Loop

CRYSTAL

Type

100 MHz SC-cut (x20)

OTHER

Label

Use conventional label with the

following information:

501-28313 (Current Rev.)

1G/2G MXO-PLD

+15 VDC

Serial # - Date Code

(Mark connectors with function)

Test Data

- Output Level
- Phase Noise free-running
- Temperature Stability free-running
- Harmonics, Subs, Products, Spurious
- Power Warm-up and Total

REV	DATE	REVISION RECORD	DWN	AUTH
-	07-31-14	Initial Release	PAC	

J2P-023 MXO Connections		
Connector	Function	
1	Supply Voltage	
2	Ground, Case	
4	RF Output B	
5	Phase Lock Voltage	
6	Phase Lock Alarm	
7	External Reference Input	
8	RF Output A	



