## **EXTERNAL REFERENCE INPUT Input Frequency** 10 MHz Input Level +7 dBm ±6 dB into 50 ohms **OUTPUT Output Frequency** 128 MHz **Output Level** +13 dBm ±2 dB into 50 ohms STABILITY, free running Aging (typical) 1 x 10<sup>-6</sup> per year after 30 days operating per year, second year 3 x 10<sup>-7</sup> per year, thereafter Phase Noise L(f) 10 Hz -87 dBc/Hz 100 Hz -117 dBc/Hz 1 kHz -148 dBc/Hz 10 kHz -173 dBc/Hz 100 kHz -174 dBc/Hz **Temperature Stability** ± 2 x 10<sup>-7</sup>, 0° to +50°C (Ref +25°C) **SPECTRAL PURITY Harmonics** -30 dBc **PLL Divider Products** -50 dBc **Non-Harmonic Spurious** -70 dBc **MECHANICAL Packaging** Nickel-Plated Machined Aluminum Case Single Output **Dimensions** 2.5" x 3.5" x 0.8" **Connectors** RF Input/Output: SMA(f) Power, Monitoring: Feed-Thru Solder Pins **GND: Ground Turret**

**POWER REQUIREMENTS** 

≤ 5 Watts at +25°C

Target Bandwidth: ≤ 60 Hz

≤ 8 Watts for 5 minutes at +25°C

Locked: +3.5 VDC to +5.2 VDC (Hi)

Out-of-Lock: +0.8 VDC max (Lo)

Supply Voltage

Warm-Up Power

**Total Power** 

Loop BW

**CRYSTAL** 

Type

**ADJUSTMENT** 

Type 2 Loop

128 MHz SC-cut

**PHASE LOCK STATUS** 

Phase Lock Alarm (ALM)

Phase Voltage Monitor (ΦV)

+15 VDC ±5%

REV	DATE	REVISION RECORD	DWN	AUTH
-	02-04-15	Initial Release	CB	



