INPUT
Frequency <sub>7</sub>
10 MHz, ±5 x 10 <sup>-7</sup>
Level
+7 dBm ±5 dB into 50 ohms
OUTPUT
Frequency
80 MHz
Level
+13 dBm ±2 dB into 50 ohms
STABILITY
Aging
1 x 10 <sup>-6</sup> first year
after 30 days operating, typical
5 x 10 <sup>-7</sup> second year, typical
3 x 10 <sup>-7</sup> per year thereafter, typical
Phase Noise L(f), static, free-running
100 Hz -130 dBc/Hz
1 KHZ -158 GBC/HZ
10 kHz -176 dBc/Hz
100 kHz -176 dBc/Hz
Temperature Stability
Temperature otability
$\pm 5 \times 10^{-7}$ free-running from 0 to $\pm 50^{\circ}$ C,
(Ref. +25°C)
Harmonics
-30 dBc
Sub-Harmonics
-80 dBc
PLL Divider Products
-80 dBc
Non-Harmonic Spurious
-80 dBc, excluding power
supply line related spurs
Phase Lock Alarm
TTL
Locked: +3.5 VDC to +5.2 VDC (Hi)
Out-of-Lock: +0.8 VDC max (Lo)
Phase Lock Voltage Monitor
Voltage monitor pin supplied
MECHANICAL
Dimensions
2.8" x 3.0" x 1.75"
Connectors
SMA(f) and solder pins on side
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De also vin v	EV	
Packaging	-	
Nickel-plated machined		
aluminum case – CVPLOI		
POWER REQUIREMENTS		
Warm-Up Power		
≤ 8 Watts for 5 minutes		
Total Power		
≤ 5 Watts at +25°C		
Supply Voltage	3.0	0-
+15 VDC ±5%		
ADJUSTMENT		
Loop BW		
Target Bandwidth: ≤ 10 Hz		
Type 2 Loop		
CRYSTAL		
Type	0.0	n_
80 MHz SC-cut (low-g)	0.0	u –
Acceleration Sensitivity		
≤ 3 x 10 <sup>-10</sup> /g per axis, typical		
ENVIRONMENTAL		8
Operating Temperature	1.7	5_
0° to +50°C	3X 1.5 2X 1.4	<b>4</b> ∼ 1 −
Storage Temperature	1.29	9
-40° to +85°C		
Resonance		
(Internal Mount Natural Frequency)	0.0	0 –
~30 Hz, typical		
OTHER		9
Label		č
Use conventional label with the	0.00 2X 0.08	<u>0</u> –
following information:	ZA 0.00	_

## OTH Lab

following information: 501-28759 (Current Rev.) 80 MHz PL Citrine +15 VDC Serial # - Date Code

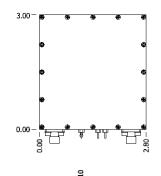
## **Test Data**

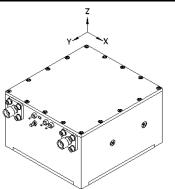
**Output Level** Phase Noise, Static, Free-Running Temperature Stability, Free-Running Harmonics, Subs, Products, Spurious Power – Warm-up and Total

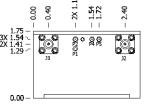
**Acceleration Sensitivity** Mount Natural Frequency Resonance

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(f) and colden since an aide		

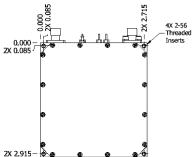
EV	DATE	REVISION RECORD	DWN	AUTH
-	02-09-15	Initial Release	BH	
	·			











CVPLOI						
Connector	Function					
P1 P2 P4 P5 J1 J2	Ground, Case Supply Voltage Phase Voltage Monitor Phase Lock Alarm External Reference Input RF Output					
Connector numbers are for reference						

only and will not be marked on unit.

Title:

## Wenzel Associates, Inc.

Premium 80 MHz-SC Phase Lock Citrine Vibration Isolated Crystal Oscillator

	Vibration isolated Crystal Oscillator									
	501-28759		Rev: Date: 02-09-15		Date:		Drawn:		Ref: ULN	
	Tolerances: (except as noted) Dimensions are in inches	0.XX Dec: ±0.030"		0"	0.XXX Dec: ±0.010"	FSG 6	<sup>см:</sup> 2821	Р	age 1 of 1	