INPUT Frequency 10 MHz Level +7 dBm ±6 dB into 50 ohms OUTPUTS Output Level **RF** Output Frequency (into 50 ohms) А 250 MHz +13 dBm ±2 dB В 1 GHz +13 dBm ±2 dB STABILITY Aging (free-running) 1 x 10⁻⁶ first year after 30 days operating, typical 5×10^{-7} second year, typical 3×10^{-7} per year thereafter, typical Phase Noise L(f), dBc/Hz, typical, (free-running) 1 GHz 250 MHz 10 Hz -83 -70 100 Hz -113 -100 -143 -129 1 kHz 10 kHz -167 -153 100 kHz -168 -154 1 MHz -168 -154 **Temperature Stability** $\pm 5 \times 10^{-7}$ free-running from 0 to $\pm 50^{\circ}$ C (Ref. +25°C) Harmonics -25 dBc Sub-Harmonics -60 dBc **PLL Divider Products** -60 dBc Spurious -80 dBc, excluding power supply line related spurs Phase Lock Alarm TTL Locked: +3.5 VDC to +5.2 VDC (Hi) Out-of-Lock: +0.8 VDC max (Lo)

	REV	DATE	REVISION RECORD				DWN	AUTH
Phase Lock Voltage Monitor	-	06-04-15	Initial Rel				CB	AUTH
Voltage monitor pin supplied								
MECHANICAL								
Dimensions								
4.4 x 4 x 1"								
Connectors								
RF Input/Outputs: SMA(f)			_					
Power, Monitoring: Feed Thru Terminals			_		3 MXO Connections			
GND: Ground Turret			_	Connector 1	Function Supply Voltage			
Packaging				2	Ground, Case RF Output B			
Nickel-plated machined				5 6	Phase Lock Voltage Phase Lock Alarm			
aluminum housing – J2P-13				7 8	External Reference In RF Output A	put		
Mounting								
Threaded inserts on base,		1.00 0.75	- 000	1 🖸				
#2-56, 6 places		0.44	- 5 6 - 007	z ©	8	4		
POWER REQUIREMENTS		0.25 0				<u> </u>		
Warm-Up Power			0 0.64 0.68	1.45 —	2.90 —	3.85 — 4.40 —		
≤ 12 Watts for 5 minutes			ວອີອັ ກີກ					
Total Power		4.00		<u>_</u>				
≤ 9 Watts at +25°C		4.00						
Supply Voltage								
+15 VDC ±5%								
ADJUSTMENT								
Loop BW								
Target Bandwidth: ≤ 10 Hz		2.000	-0			0		
Type 2 Loop								
CRYSTAL								
			π	readed Inserts,	#2-56 <i>,</i>			
125 MHz SC-cut (x2, x2, x2)		0.005		places, 0.190" d	æp			
OTHER		0.085 0	<u> 16</u>					
Label			0.085 -			4.315 -		
Use conventional label with the			ö			4, 4		
following information:								
501-29103 (Current Rev.)								
250M/1G MXO-PLD								
+15 VDC Serial # - Date Code								
(Mark connectors with function) Test Data								
- Output Level			Nenzo	el As	ssociate	es, Inc.	1	
- Output Level - Phase Noise – free-running					ustin, Texas	.,		
- Temperature Stability – free-running	Title:		-					
- Temperature Stability – nee-running - Harmonics, Subs, Products, Spurious	250 MHz & 1 GHz							
- Power – Warm-up and Total		Multi			l Oscillato			
- Fower – Warn-up and Total	P/N:	4 00400	Rev			Drawn:	R	ef:
	50	1-29103	-)6-04-15			
	Tolerances (except as		0.XX Dec:		0.XXX Dec:	FSCM:	4	4
		are in inches	±0.03	0 "	±0.010"	62821	Page 1	of
	1						1	