

INPUT

Frequency

10 MHz, $\pm 2 \times 10^{-7}$

Level

+7 dBm ± 6 dB into 50 ohms

OUTPUT

Frequency

10 MHz

Level

+13 dBm ± 2 dB into 50 ohms

STABILITY

Aging

5×10^{-10} per day
after 30 days operating, typical

Phase Noise L(f), static, free-running

10 Hz -130 dBc/Hz

100 Hz -155 dBc/Hz

1 kHz -165 dBc/Hz

10 kHz -165 dBc/Hz

Temperature Stability

$\pm 5 \times 10^{-8}$ free-running from 0 to +50°C,
(Ref. +25°C)

Harmonics

-30 dBc

PLL Divider Products

-80 dBc

Non-Harmonic Spurious

-80 dBc, excluding power
supply line related spurs

Phase Lock Alarm

TTL

Locked: +3.5 VDC to +5.2 VDC (Hi)

Out-of-Lock: +0.8 VDC max (Lo)

Phase Lock Voltage Monitor

Voltage monitor pin supplied

MECHANICAL

Dimensions

2.25 x 2.25 x 1.3"

Connectors

SMA(f) and solder pins on side

Packaging

Nickel-plated machined
aluminum case - CHPLO

POWER REQUIREMENTS

Warm-Up Power

≤ 8 Watts for 5 minutes at +25°C

Total Power

≤ 5 Watts at +25°C

Supply Voltage

+15 VDC $\pm 5\%$

ADJUSTMENT

Loop BW

Target Bandwidth: ≤ 1 Hz

Type 2 Loop

CRYSTAL

Type

10 MHz SC-cut

Acceleration Sensitivity

$\leq 5 \times 10^{-10}$ /g per axis, typical

ENVIRONMENTAL

Operating Temperature

0° to +50°C

Storage Temperature

-40° to +85°C

OTHER

Label

Use conventional label with the
following information:

501-29284 (Current Rev.)

10 MHz Citrine PLO

+15 VDC

Serial # - Date Code

Test Data

Output Level

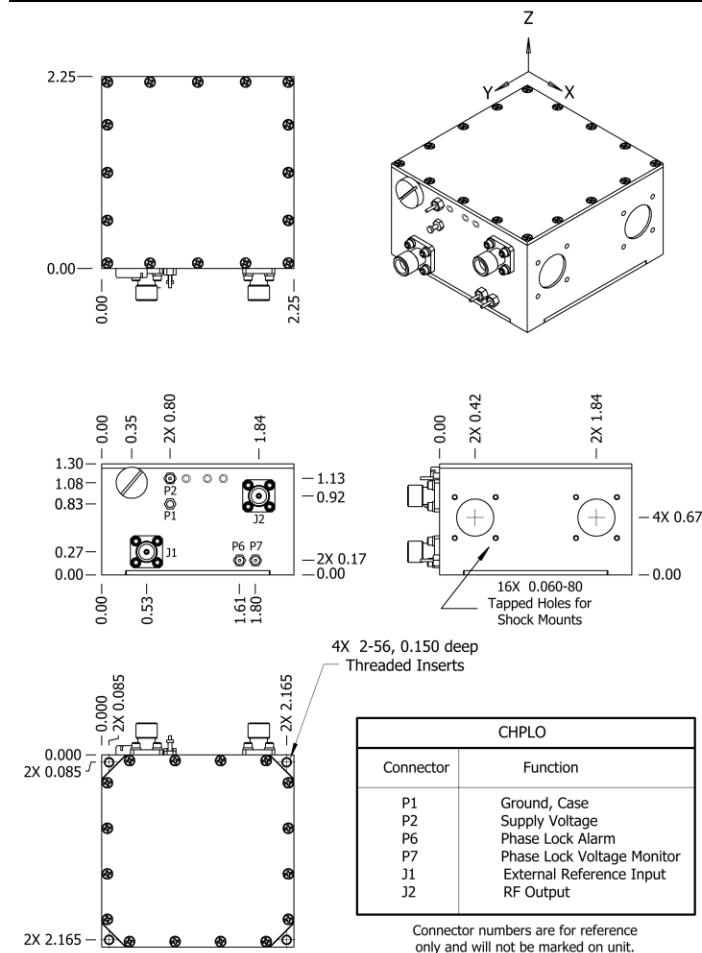
Phase Noise, Static, Free-Running

Temperature Stability, Free-Running

Harmonics, PLL Products, Spurious

Power – Warm-up and Total

REV	DATE	REVISION RECORD	DWN	AUTH
-	07-22-15	Initial Release	PAC	



Wenzel Associates, Inc.

Austin, Texas

Title:

Standard 10 MHz-SC Phase Lock Citrine Crystal Oscillator

P/N:

501-29284

Rev:

-

Date:

07-22-15

Drawn:

Ref:

STR

Tolerances:
(except as noted)
Dimensions are in inches

0.XX Dec:

± 0.030 "

0.XXX Dec:

± 0.010 "

FSCM:

62821

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