		REV	REV DATE REVISION RECORD			DWN	AUTH	
INPUT	MECHANICAL	- 10-13-15 Initial Release			Liz			
Frequency	Dimensions	А	A 10-18-15 10K, 30K noise			Liz		
10 MHz	4.4 x 4 x 1"							
Level	Connectors							
+7 dBm ±6 dB into 50 ohms	RF Input/Output: SMA(f)							
OUTPUT	Power, Monitoring: Feed Thru Terminals			120 1	1XO Connections			
Frequency	GND: Ground Turret					_		
1 GHz	Packaging			Connector	Function	_		
Level	Nickel-plated machined			1 2	Supply Voltage Ground, Case			
+17 dBm ±2 dB into 50 ohms	aluminum housing – J2P			4	RF Output			
STABILITY	Mounting			6	Phase Lock Voltage Phase Lock Alarm			
Aging (free-running)	Threaded inserts on base,			7	External Reference Input	t		
1 x 10 ⁻⁶ first year	#2-56, 6 places							
after 30 days operating, typical	POWER REQUIREMENTS	1.	00 —				3	
5×10^{-7} second year, typical	Warm-Up Power	0.	75 - 0 0	9 10				
3×10^{-7} per year thereafter, typical	\leq 14 Watts for 5 minutes	0.	44 - 5	6 2 ©		4		
Phase Noise L(f), typical	Total Power							
10 Hz -86 dBc/Hz	≤ 10.5 Watts at +25°C		0 -	5 I			3	
100 Hz -113 dBc/Hz	Supply Voltage		0.40	0.68 - 1.45 -		3.85	\$	
1 KHz -134 dBc/Hz	+15 VDC ±5%		66			m 	4	
10 KHz -150 dBc/Hz	ADJUSTMENT							
30 KHz -151 dBc/Hz	Loop BW	3.9	00 — 0 15 - 0				२	
100 KHz -153 dBc/Hz	Target Bandwidth: ≤ 10 Hz							
1 MHz -172 dBc/Hz	Type 2 Loop							
3 MHz -174 dBc/Hz	CRYSTAL							
10 MHz -174 dBc/Hz	Туре							
Temperature Stability	100 MHz SC-cut (x10)							
$\pm 5 \times 10^{-7}$ free-running from 0 to	OTHER		L			_		
	Design	2.0	00 - 0			(2	
+50°C (Ref. +25°C)	Includes a SAW filter for improving the							
Harmonics	phase noise beyond the ~300 kHz offset.							
-25 dBc Sub-Harmonics	Label							
	Use conventional label with the							
-60 dBc PLL Divider Products	following information:	Threaded Inserts, #2-56, 6 places, 0.190" deep						
-60 dBc	501-29465 (Current Rev.)	0.0	85 7					
Spurious	1 GHz GQMXO-PLD	5.0				/	2 -	
-80 dBc, excluding power	+15 VDC		0 - 0			15 -	4.40	
supply line related spurs	Serial # - Date Code		0.0			4.3	4	
Phase Lock Alarm	(Mark connectors with function)	Wenzel Associates, Inc.						
TTL	Test Data - Output Level	Austin, Texas						
Locked: +3.5 VDC to +5.2 VDC (Hi)	- Phase Noise – free-running	Title:						
Out-of-Lock: +0.8 VDC max (Lo)	- Temperature Stability – free-running	1 GHz Golden-Q Multiplied						
Phase Lock Voltage Monitor	- Harmonics, Subs, Products, Spurious	Crystal Oscillator (GQMXO-PLD)						
Voltage monitor pin supplied	- Power – Warm-up and Total	P/N:	00405			wn:	Ref:	5722
· · · · · · · · · · · · · · · · · · ·		50	1-29465	A	10-18-15			.5122
		Tolerances (except as		0.XX Dec:		SCM:	4	4
			s are in inches	±0.030"	±0.010"	62821	Page 1 o	ot I