INPUT	MECHANICAL
Frequency	Dimensions
10 MHz	4.4 x 4 x 1"
Level	Connectors
+7 dBm ±6 dB into 50 ohms	RF Input/Output: SMA(f)
OUTPUT	Power, Monitoring: Feed Thru Terminals
Frequency	GND: Ground Turret
2 GHz	Packaging
Level	Nickel-plated machined
+17 dBm ±2 dB into 50 ohms	aluminum housing – J2P
STABILITY	Mounting
Aging (free-running)	Threaded inserts on base,
1 x 10 ⁻⁶ first year	#2-56, 6 places
after 30 days operating, typical	POWER REQUIREMENTS
5 x 10 ⁻⁷ second year, typical	Warm-Up Power
	≤ 13.5 Watts for 5 minutes
3 x 10 ⁻⁷ per year thereafter, typical	Total Power
Phase Noise L(f), typical	≤ 10.5 Watts at +25°C
10 Hz -79 dBc/Hz	Supply Voltage
100 Hz -106 dBc/Hz	+15 VDC ±5%
1 KHz -127 dBc/Hz	ADJUSTMENT
10 KH z -143 dBc/Hz	Loop BW
30 KHz -144 dBc/Hz	Target Bandwidth: ≤ 10 Hz
100 KHz -146 dBc/Hz	Type 2 Loop
1 MHz -162 dBc/Hz 3 MHz -164 dBc/Hz 10 MHz -164 dBc/Hz	CRYSTAL
3 MHZ -164 dBC/HZ	Туре
	100 MHz SC-cut (x20)
Temperature Stability	OTHER
$\pm 5 \times 10^{-7}$ free-running from 0 to	Design
+50°C (Ref. +25°C)	Includes a SAW filter for improving the
Harmonics	phase noise beyond the ~300 kHz offset.
-25 dBc	Label
Sub-Harmonics	Use conventional label with the
-60 dBc	following information:
PLL Divider Products	501-29477 (Current Rev.)
-60 dBc	2 GHz GQMXO-PLD
Spurious	+15 VDC
-80 dBc, excluding power	Serial # - Date Code
supply line related spurs	(Mark connectors with function)
Phase Lock Alarm	Test Data
· · -	- Output Level
Locked: +3.5 VDC to +5.2 VDC (Hi)	- Phase Noise – free-running
Out-of-Lock: +0.8 VDC max (Lo) Phase Lock Voltage Monitor	- Temperature Stability – free-running
Voltage monitor pin supplied	- Harmonics, Subs, Products, Spurious
voltage monitor pin supplied	- Power – Warm-up and Total

	REV	DATE	REVISION RECORD	DWN	AUTH
I		10-18-15	Initial Release	Liz	
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J2P	J2P MXO Connections		
Connector	Function		
1 2 4 5 6 7	Supply Voltage Ground, Case RF Output Phase Lock Voltage Phase Lock Alarrn External Reference Input		



