INPUT	MECHANICAL
Frequency	Dimensions
10 MHz	4.4 x 4 x 1"
Level	Connectors
+7 dBm ±6 dB into 50 ohms	RF Input/Output: SMA(f)
OUTPUT	Power, Monitoring: Feed Thru Terminals
Frequency	GND: Ground Turret
3 GHz	Packaging
Level	Nickel-plated machined
+17 dBm ±2 dB into 50 ohms	aluminum housing – J2P
STABILITY	Mounting
Aging (free-running)	Threaded inserts on base,
	#2-56, 6 places
1 x 10 ⁻⁶ first year	POWER REQUIREMENTS
after 30 days operating, typical	Warm-Up Power
5 x 10 ⁻⁷ second year, typical	≤ 14.5 Watts for 5 minutes
3×10^{-7} per year thereafter, typical	Total Power
Phase Noise L(f), typical	≤ 11 Watts at +25°C
10 Hz -75 dBc/Hz	
100 Hz -102 dBc/Hz	Supply Voltage
1 KHz -123 dBc/Hz	+15 VDC ±5%
10 KHz -139 dBc/Hz	ADJUSTMENT
30 KHz -140 dBc/Hz	Loop BW
30 KHz -140 dBc/Hz 100 KHz -142 dBc/Hz 1 MHz -158 dBc/Hz	Target Bandwidth: ≤ 10 Hz
1 MHz -158 dBc/Hz	Type 2 Loop
3 MHz -160 dBc/Hz	CRYSTAL
10 MHz -160 dBc/Hz	Type
Temperature Stability	100 MHz SC-cut (x30)
±5 x 10 ⁻⁷ free-running from 0 to	OTHER
	Design
+50°C (Ref. +25°C) Harmonics	Includes a SAW filter for improving the
	phase noise beyond the ~300 kHz offset.
-25 dBc	Label
Sub-Harmonics	Use conventional label with the
-60 dBc	following information:
PLL Divider Products	501-29478 (Current Rev.)
-60 dBc	3 GHz GQMXO-PLD
Spurious	+15 VDC
-80 dBc, excluding power	Serial # - Date Code
supply line related spurs	(Mark connectors with function)
Phase Lock Alarm	Test Data
TTL	- Output Level
Locked: +3.5 VDC to +5.2 VDC (Hi)	- Phase Noise – free-running
Out-of-Lock: +0.8 VDC max (Lo)	 Temperature Stability – free-running
Phase Lock Voltage Monitor Voltage monitor pin supplied	 Harmonics, Subs, Products, Spurious

REV	DATE	REVISION RECORD	DWN	AUTH
-	10-18-15	Initial Release	Liz	

J2P MXO Connections		
Connector Function		
1 2 4 5 6 7	Supply Voltage Ground, Case RF Output Phase Lock Voltage Phase Lock Alarrn External Reference Input	



