		REV	DATE	F	REVISION RECORD	DWN	AUTH
	MECHANICAL	-	10-18-15	Initial Release		Liz	
	MECHANICAL					1	
Frequency	Dimensions						
10 MHz	5.36 x 4 x 1"						
Level	Connectors						
+7 dBm ±6 dB into 50 ohms	RF Input/Output: SMA(f)						
OUTPUT	Power, Monitoring: Feed Thru Terminals			J3F	P MXO Connections		
Frequency	GND: Ground Turret			Connector	Function		
6.0 GHz	Packaging			1	Supply Voltage		
Level	Nickel-plated machined			2	Ground, Case RF Output		
+17 dBm ±2 dB into 50 ohms	aluminum housing – J3P			5	Phase Lock Voltage		
STABILITY	Mounting			5	Phase Lock Alarm External Reference Input		
Aging (free-running)	Threaded inserts on base,				•		
1 x 10 ⁻⁶ first year	#2-56, 6 places						
after 30 days operating, typical	POWER REQUIREMENTS	1.00				0-0	7
5×10^{-7} second year, typical	Warm-Up Power	0.75		1 🖸		Ţ,	
	≤ 16 Watts for 5 minutes	0.44	-	2 🔘		4	
3×10^{-7} per year thereafter, typical	Total Power	0.25	- 700	9			
Phase Noise L(f), typical	≤ 13 Watts at +25°C	0	一 T 占				
10 Hz -69 dBc/Hz	Supply Voltage		0.40 0.640 0.684	1.45		4.81	5.36
100 Hz -96 dBc/Hz	+15 VDC ±5%		n			è	
1 KHz -117 dBc/Hz	ADJUSTMENT	4.00				_ /	5
10 KHz -133 dBc/Hz	Loop BW	3.915	- 1 <u>9</u>			- le	
30 KHz -134 dBc/Hz	Target Bandwidth: ≤ 10 Hz						
100 KHz -136 dBc/Hz	Type 2 Loop						
1 MHz -152 dBc/Hz	CRYSTAL						
3 MHz -154 dBc/Hz	Туре						
10 MHz -154 dBc/Hz	100 MHz SC-cut (x60)						
Temperature Stability	OTHER					_	
±5 x 10 ⁻⁷ free-running from 0 to +50°C	Design	2.000	$ \bigcirc$			(2
(Ref. +25°C)	Includes a SAW filter for improving the						
Harmonics	phase noise beyond the ~300 kHz offset						
-25 dBc	Label						
Sub-Harmonics	Use conventional label with the						
-60 dBc			T	hreaded Inserts, #2-56, places, 0.190" deep			
PLL Divider Products	following information:			places, 0.150 accp			
-60 dBc	500-29481 (Current Rev.)	0.085 0				/0	5
	6.0 GHz MXO-PLD	U	4				4
Spurious	+15 VDC		0.085			5.270	5.36
-80 dBc, excluding power	Serial # - Date Code		ö			ις.	
supply line related spurs	(Mark connectors with function)			Nonzal	Accoriatos Ino		
Phase Lock Alarm	Test Data						
	- Output Level	Title:					
Locked: +3.5 VDC to +5.2 VDC (Hi)	- Phase Noise – free-running	6.0 GHz Multiplied Crystal Oscillator (MXO-PLD)					
Out-of-Lock: +0.8 VDC max (Lo)	 Temperature Stability – free-running 						
Phase Lock Voltage Monitor	- Harmonics, Subs, Products, Spurious	P/N: Rev: Date: Drawn: Ref:					
Voltage monitor pin supplied	 Power – Warm-up and Total 	50	0-29481	-	10-18-15		
		Tolerances		0.XX Dec:	0.XXX Dec: FSCM:		
		(except as Dimensions	noted) s are in inches	±0.030"	±0.010" 62821	Page 1 c	of 1
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