		REV	DATE	REVISION RECORD	DWN	AUTH
INPUT	MECHANICAL	-	10-18-15	Initial Release	Liz	-
	Dimensions					
	$5.36 \times 4 \times 1^{\circ}$					
10 MHz						
Level	Connectors					
+7 dBm ±6 dB into 50 ohms	RF Input/Output: SMA(f)					
OUTPUT	Power, Monitoring: Feed Thru Terminals			J3P MXO Connections		
Frequency	GND: Ground Turret			Connector Function		
10.0 GHz	Packaging			1 Supply Voltage 2 Ground, Case		
Level	Nickel-plated machined			4 RF Output		
+17 dBm \pm 2 dB into 50 ohms	aluminum housing – J3P			5 Phase Lock Voltage 6 Phase Lock Alarm		
STABILITY	Mounting			7 External Reference Input		
Aging (free-running)	Threaded inserts on base,					
1 x 10 ⁻⁶ first year	#2-56, 6 places	1.00				
after 30 days operating, typical	POWER REQUIREMENTS	0.75	$- \bigcirc \bigcirc_{5} \bigcirc_{6}$	1 💿		
5×10^{-7} second year, typical	Warm-Up Power	0.44	- <u>5</u> - <u>7</u>	2 🛇	4	
	≤ 16 Watts for 5 minutes	0.25	7 00			
3 x 10 ⁻⁷ per year thereafter, typical	Total Power	0				
Phase Noise L(f), typical	≤ 13 Watts at +25°C		0 0.40 0.64 0.68	1.45	4.81	
10 Hz -65 dBc/Hz	Supply Voltage			A	\square	
100 Hz -92 dBc/Hz	+15 VDC ±5%	4.00		<u>_</u>		
1 KHz -113 dBc/Hz	ADJUSTMENT	3.915				
10 KHz -129 dBc/Hz	Loop BW					
30 KHz -130 dBc/Hz	Target Bandwidth: ≤ 10 Hz					
100 KHz -132 dBc/Hz	Type 2 Loop					
1 MHz -148 dBc/Hz	CRYSTAL					
3 MHz -150 dBc/Hz	Туре					
10 MHz -150 dBc/Hz	100 MHz SC-cut (x100)	2.000	-0		0	
Temperature Stability	OTHER					
$\pm 5 \times 10^{-7}$ free-running from 0 to $\pm 50^{\circ}$ C	Design					
(Ref. +25°C)	Includes a SAW filter for improving the					
Harmonics	phase noise beyond the ~300 kHz offset					
-25 dBc	Label			readed Inserts, #2-56 laces, 0.15" deep		
Sub-Harmonics	Use conventional label with the	0.085		aces, 0.15 deep	6	
-60 dBc		0	_0			
PLL Divider Products	following information:		0.085		5.270	
-60 dBc	501-29482 (Current Rev.)		ō		<u>о</u>	
Spurious	10 GHz MXO-PLD					
-80 dBc, excluding power	+15 VDC					
supply line related spurs	Serial # - Date Code					
Phase Lock Alarm	(Mark connectors with function)			Nenzel Associates,	Inc	
	Test Data	Austin, Texas				
	- Output Level	Title:				
Locked: $+3.5$ VDC to $+5.2$ VDC (Hi)	- Phase Noise – free-running		GHz Mult	iplied Crystal Oscillato	or (GQMXO	-PLD)
Out-of-Lock: +0.8 VDC max (Lo)	- Temperature Stability – free-running				•	- ,
Phase Lock Voltage Monitor	- Harmonics, Subs, Products, Spurious	P/N:	01-29482		Drawn: Re	51.
Voltage monitor pin supplied	 Power – Warm-up and Total 	50	1-23402			
		Tolerance (except as	noted)	0.XX Dec: 0.XXX Dec: FSC		.1
			is are in inches	±0.030" ±0.010" 6	2821 Page 1	I TO