| | | REV | DATE | REVISION RECORD DWN AUT |
|--|--|------------|-------------------|---|
| INPUT | Connectors | - | 12-15-15 | Initial Release Liz/CB |
| Frequency | RF Input/Output: SMA(f) | | | |
| 10 MHz | Power, Monitoring: Feed Thru Terminals | | ļ | |
| Level | GND: Ground Turret | | | |
| $+7 \text{ dBm } \pm 6 \text{ dB into } 50 \text{ ohms}$ | Packaging | | | |
| OUTPUT | Nickel-plated machined | | | |
| Frequency | aluminum housing – J2P | | | |
| 3.5 GHz | Mounting | | | J2P MXO Connections |
| Level | Threaded inserts on base, | | | Connector Function |
| \pm +13 dBm \pm 2 dB into 50 ohms | #2-56, 6 places | | | 1 Supply Voltage |
| STABILITY | POWER REQUIREMENTS | | | 2 Ground, Case 4 RF Output |
| Aging (free-running) | Warm-Up Power | | | 5 Phase Lock Voltage |
| 1×10^{-6} first year | \leq 11.5 Watts for 5 minutes | | | 6 Phase Lock Alarm 7 External Reference Input |
| | Total Power | | | |
| after 30 days operating, typical | ≤ 9 Watts at +25°C | | 1.00 — r | |
| 5×10^{-7} second year, typical | Supply Voltage | | 0.75 — | |
| 3×10^{-7} per year thereafter, typical | +12 VDC ±5% | | 0.44 — | 5 6 . 4 |
| Phase Noise L(f), typical, (free-running) | ADJUSTMENT | | 0.25 — | |
| 100 Hz -93 dBc/Hz | Loop BW | | 0 — E | |
| 1 KHz -120 dBc/Hz | Target Bandwidth: ≤ 60 Hz | | c | 0.40 0.654 3.385 4.45 6.654 4.5 6.640 4.45 |
| 10 KHz -137 dBc/Hz | Type 2 Loop | | | |
| 100 KHz -138 dBc/Hz | CRYSTAL | | 4.00 | |
| Temperature Stability | Туре | | 4.00 — 3.915 ∽ | * t |
| ±5 x 10 ⁻⁷ free-running from 0 to +70°C | 70 MHz SC-cut (x50) | | | |
| (Ref. +25°C) | OTHER | | | |
| Harmonics | Label | | | |
| -25 dBc | Use conventional label with the | | | |
| Sub-Harmonics | following information: | | | |
| -60 dBc | 501-29504 (Current Rev.) | | 2.000 — | D 0 |
| PLL Divider Products | 3.5 GHz MXO-PLD | | | |
| -60 dBc | +12 VDC | | | |
| Spurious | Serial # - Date Code | | | |
| -80 dBc, excluding power | (Mark connectors with function) | | | |
| supply line related spurs | Test Data | | | Threaded Inserts, #2-56, 6 Places, 0.150° deep |
| Phase Lock Alarm | - Output Level | | 0.085 | |
| TTL | - Phase Noise – free-running | | 0 — L | |
| Locked: +3.5 VDC to +5.2 VDC (Hi) | - Temperature Stability – free-running | | c | 0.085 4.33 4.40 5 |
| Out-of-Lock: +0.8 VDC max (Lo) | - Harmonics, Subs, Products, Spurious | | | 0 4 ⁴ . |
| Phase Lock Voltage Monitor | - Power – Warm-up and Total | | | |
| Voltage monitor pin supplied | | | | Venzel Associates, Inc. |
| MECHANICAL | | | | Austin, Texas |
| Dimensions | | Title: | <u> </u> | |
| 4.40 x 4 x 1" | | 3.5 | GHz Mu | Itiplied Crystal Oscillator (MXO-PLD) |
| | | P/N: | | Rev: Date: Drawn: Ref: |
| | | 50 | 1-29504 | - 12-15-15 |
| | | Tolerances | : | 0.XX Dec: 0.XXX Dec: FSCM: |
| | | (except as | | ±0.030" ±0.010" 62821 Page 1 of 1 |
| | | | | |