OUTPUT Frequency 10 MHz Level +4 dBm ±3 dB into 50 ohms **EXTERNAL REFERENCE INPUT** Frequency 5 or 10 MHz, ±3 Hz selectable Level +4 dBm ±3dB into 50 ohms VSWR 1.5:1 max Phase Noise L(f), typical at 10 MHz -85 dBc 1 Hz 10 Hz -117 dBc 100 Hz -132 dBc 1 KHz -142 dBc 10 KHz -147 dBc **OUTPUT STABILITY** Aging 1×10^{-9} /day after 24 hours operating preceeded by 3 days of operation. Disciplined. Phase Noise L(f) 1 Hz -100 dBc 10 Hz -130 dBc 100 Hz -155 dBc 1 KHz -163 dBc **Temperature Stability** $\pm 5 \times 10^{-9}$, 0° to ± 65 °C total change Type 2, 3rd order PLL BW @ 0.05 Hz, nominal Input Output PLL Rejection 1 Hz 15 dB Min. 10 Hz 35 dB Min 100 Hz 55 dB Min

MECHANICAL Dimensions 2.26" x 2.26" x 0.61" max Connectors SMA Output, SMA Input S4B-PH-K-S-(LF)(SN) Control/Status S6B-PH-K-S-(LF)(SN) Programming Packaging Nickel plated aluminum housing POWER REQUIREMENTS Warm-Up Power 7.5 Watts for 5 minutes **Total Power** 3.5 Watts at +25°C Supply Voltage +12 VDC +-5% CRYSTAL 10 MHz SC-cut, ≥90°C STATUS Pin – Locked status TTL, High = locked TTL, Low = Not locked OTHER **Test Data** Output Level, Phase Noise freerunning and with external input. Smooth transition verification. **Frequency Calibration** Manual Calibration enable: TTL-Low 0V Calibration not enabled: TTL-High +5V **Slow Transition** Smooth frequency transitions on the loss and acquisition of reference input Disciplined Will manually learn reference frequency with Cal Pin (See operation notes) Begins initial aging correction after being locked for 14 days Tit P/I

REV	DATE	REVISION RECORD	DWN	AUTH
-	06-07-16	Initial Release	Liz	
	REV			

Wenzel Associates, Inc.											
Title: 10 MHz-SC Digital Phase Locked Oscillator with Smooth Frequency Transitions (RoHS)											
P/N:	-	Rev:			Drawn:		Ref:				
501-30108		-	06-07-16				500-25237e				
Tolerances: (except as noted) Dimensions are in inches	-	.XX Dec: ±0.03	0"	0.XXX Dec: ±0.010"	FSCM: 62821	F	Page 1 of 2				

