		REV	DATE	REVISION RECORD	DWN	AUTH
INPUT	MECHANICAL	-	09-07-16	Initial Release	CB	
-	MECHANICAL					
Frequency 10 MHz	Dimensions 4.4 x 4 x 1"					
Level	Connectors					
+7 dBm ±6 dB into 50 ohms	RF Input/Output: SMA(f)					
OUTPUT	Power, Monitoring: Feed Thru Terminals			J2P-03 MXO Connections		
	GND: Ground Turret			Connector Function		
RF Output Frequency Output Level (into 50 ohms)	Packaging			1 Supply Voltage		
· · · · · · · · · · · · · · · · · · ·	Nickel-plated machined			2 Ground, Case 4 RF Output B		
A 100 MHz +13 dBm ±2 dB	aluminum housing – J2P-03			5 Phase Lock Voltage 6 Phase Lock Alarm		
B 5 GHz +13 dBm ±2 dB	Mounting			7 External Reference Input 8 RF Output A		
STABILITY	Threaded inserts on base,					
Aging (free-running)	#2-56, 6 places		1.00 — 0.75 —	00 10		
1 x 10 ⁻⁶ first year	POWER REQUIREMENTS		0.44 —	5 6 4		
after 30 days operating, typical	Warm-Up Power		0.25 —			
5 x 10 ⁻⁷ second year, typical	≤ 14 Watts for 5 minutes		0 —			
3×10^{-7} per year thereafter, typical	Total Power		5	0.40 0.68 0.68 0.68 0.68 0.68 0.68 0.68 0.6	f	
Phase Noise L(f), typical, (free-running)	≤ 10.5 Watts at +25°C		4.00			
100 MHz 5 GHz	Supply Voltage +15 VDC ±5%		4.00 — 3.915 - 9			
	ADJUSTMENT					
10 Hz -100 -63	Loop BW					
100 Hz -130 -93	Target Bandwidth: ≤ 10 Hz					
1 kHz -158 -120	Type 2 Loop					
10 kHz -175 -137	CRYSTAL		2.000 - 0	0		
100 kHz -176 -138	Туре		2.000 -0			
Tomporatura Stability	100 MHz SC-cut (x50)					
Temperature Stability	OTHER					
$\pm 5 \times 10^{-7}$ free-running from 0 to $\pm 50^{\circ}$ C	Label			Threaded Inserts, #2-56,		
(Ref. +25°C) Harmonics	Use conventional label with the			6 places, 0.190" deep		
-25 dBc	following information:		0.085 -	· /o		
Sub-Harmonics	501-30312 (Current Rev.)		<u>ا</u> لے ہ			
-60 dBc	100M/5G MXO-PLD		0.085			
PLL Divider Products	+15 VDC					
-60 dBc	Serial # - Date Code		0	1		
Spurious	(Mark connectors with function)			8		
-80 dBc, excluding power	Test Data - Output Level		0.75 —	J. J		
supply line related spurs	- Phase Noise – free-running					
Phase Lock Alarm	- Temperature Stability – free-running			Wenzel Associates, Inc		
TTL	- Harmonics, Subs, Products, Spurious			Austin, Texas		
Locked: +3.5 VDC to +5.2 VDC (Hi)	- Power – Warm-up and Total	Title:		ultiplied Operated Operations		
Out-of-Lock: +0.8 VDC max (Lo)		100	11VI/5G IVI	ultiplied Crystal Oscillator		PLD)
Phase Lock Voltage Monitor		P/N:		Rev: Date: Drawn:	Re	ef:
Voltage monitor pin supplied		50)1-30312	2 - 09-07-16		
		Tolerances (except as		0.XX Dec: 0.XXX Dec: FSCM: ±0.030" ±0.010" 62821	Page 1	4