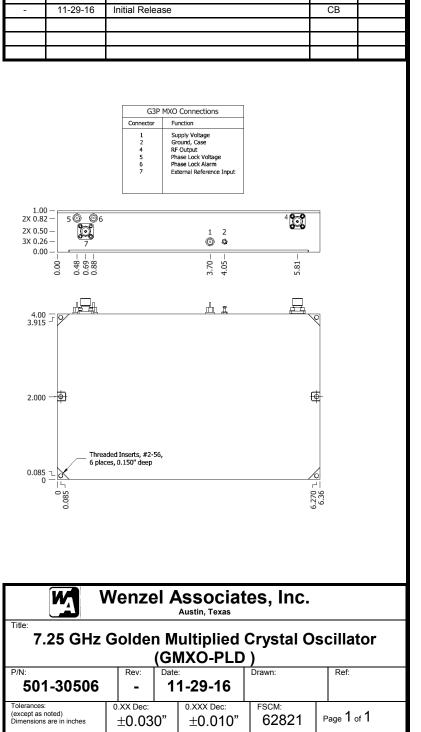
INPUT Frequency 10 MHz Level +7 dBm ±6 dB into 50 ohms OUTPUT Frequency 7.25 GHz Level +13 dBm ±2 dB into 50 ohms STABILITY Aging (free-running) 1 x 10⁻⁶ first year after 30 days operating, typical 5×10^{-7} second year, typical 3×10^{-7} per year thereafter, typical Output Phase Noise L(f) (Free-Running, typical) 100 Hz -91 dBc/Hz 1 KHz -115 dBc/Hz 10 KHz -136 dBc/Hz 100 KHz -138 dBc/Hz 1 MHz -138 dBc/Hz **Temperature Stability** $\pm 5 \times 10^{-7}$ free-running from 0 to $\pm 50^{\circ}$ C (Ref. +25°C) Harmonics ≤ -25 dBc Sub-Harmonics ≤ -60 dBc PLL Divider Products ≤ -60 dBc Spurious ≤ -80 dBc, excluding power supply line related spurs **MECHANICAL** Dimensions 6.36 x 4 x 1" Connectors RF Outputs: SMA(f) Power, ET: Feed Thru Terminals GND: Ground Turret Packaging Nickel-plated machined aluminum housing - G3P

Mountina Threaded inserts on base, #2-56. 6 places POWER REQUIREMENTS Warm-Up Power ≤ 22 Watts for 5 minutes **Total Power** ≤ 18 Watts at +25°C Supply Voltage +15 VDC ±5% ADJUSTMENT Loop BW Target Bandwidth: < 10 Hz Type 2 Loop PHASE LOCK STATUS Phase Lock Alarm TTL Locked: +3.5 VDC to +5.2 VDC (Hi) Out-of-Lock: +0.8 VDC max (Lo) Phase Lock Voltage Monitor Electrical tuning monitor pin supplied CRYSTAL Type 72.5 MHz SC-cut (x100) OTHER Label Use conventional label with the following information: 501-30506 (Current Rev.) 7.25 GHz GMXO-PLD +15 VDC Serial # - Date Code (Mark connectors with function) Test Data **Output Level** Phase Noise - free-running Temperature Stability (free-running) Harmonics, Subs, Products, Spurious Power – Warm-up and Total



REVISION RECORD

DWN

AUTH

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DATE