		REV	DATE	REVISION RECORD	DWN	AUTH
INPUT	Connectors	-	03-14-17	Initial Release	CB	
Frequency	RF Input/Output: SMA(f)					
10 MHz	Power, Monitoring: Feed Thru Terminals					
Level	GND: Ground Turret					
+7 dBm ±6 dB into 50 ohms	Packaging					
OUTPUT	Nickel-plated machined					
Frequency	aluminum housing – J2P					
5.5 GHz	Mounting					
Level	Threaded inserts on base,					
	#2-56, 6 places					
+13 dBm ±2 dB into 50 ohms STABILITY	POWER REQUIREMENTS			J2P MXO Connections		
	-			Connector Function		
Aging (free-running)	Warm-Up Power			1 Supply Voltage 2 Ground, Case		
1 x 10 <sup>-6</sup> first year	≤ 14.5 Watts for 5 minutes Total Power			4 RF Output 5 Phase Lock Voltage		
after 30 days operating, typical				6 Phase Lock Alarm 7 External Reference Input		
5 x 10 <sup>-7</sup> second year, typical	≤ 11 Watts at +25°C			, Enternal reference input		
3 x 10 <sup>-7</sup> per year thereafter, typical	Supply Voltage		1.00 —			
Phase Noise L(f), typical, (free-running)	+15 VDC ±5%		0.75 —	$ \bigcirc \bigcirc 1 $		
100 Hz -83 dBc/Hz			0.44 —			
1 KHz -112 dBc/Hz	Loop BW		0.25 —			
10 KHz -136 dBc/Hz	Target Bandwidth: ≤ 10 Hz		0	0.40 - 0.68 \ - 0.68 0.68 0.68 0.68 0.68 0.47 0.47 0.47 0.440 0.440 0.440 0.440 0.440 0.440 0.440 0.440 0.440 0.440 0.440 0.440 - 0.4400 - 0.4400 - 0.4400 - 0.4400 - 0.4400 - 0.4400 - 0.4		
100 KHz -137 dBc/Hz	Type 2 Loop			0.64 0.65 0.658 0.6		
Temperature Stability	CRYSTAL		4.00 -			
$\pm 5 \times 10^{-7}$ free-running from 0 to +50°C			4.00 - 3.915 - 9	× ×		
	110 MHz SC-cut (x50)					
(Ref. +25°C) Harmonics	OTHER					
-25 dBc	Label					
Sub-Harmonics	Use conventional label with the					
	following information:		2.000 — O			
-60 dBc PLL Divider Products	501-30717 (Current Rev.)					
	5.5 GHz MXO-PLD					
-60 dBc	+15 VDC					
Spurious	Serial # - Date Code			Threaded Inserts, #2-56,		
-80 dBc, excluding power	(Mark connectors with function)			6 Places, 0.150" deep		
supply line related spurs Phase Lock Alarm	Test Data					
	- Output Level		0	- 2000 4.315		
	- Phase Noise – free-running		č	00 4 4		
Locked: $+3.5$ VDC to $+5.2$ VDC (Hi)	- Temperature Stability – free-running					
Out-of-Lock: +0.8 VDC max (Lo)	- Harmonics, Subs, Products, Spurious					
Phase Lock Voltage Monitor	- Power – Warm-up and Total			Nenzel Associates, Inc		
Voltage monitor pin supplied			MA \	Austin. Texas		
MECHANICAL		Title:		Austin, Texas		
Dimensions 4.40 x 4 x 1"			GHz Mu	Itiplied Crystal Oscillator	(MXO-F	PLD)
		P/N:		Rev: Date: Drawn:	Re	ef:
		50	1-30717	- 03-14-17	5	501-25401

Tolerances: (except as noted) Dimensions are in inches 0.XX Dec:

±0.030"

FSCM:

62821

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0.XXX Dec:

±0.010"