INPUT	MECHANICAL		
Frequency	Dimensions		
10 MHz	5.36 x 4 x 1"		
Level	Connectors		
+13 dBm ±1 dB into 50 ohms	RF Input/Output: SMA(f)		
OUTPUT	Power, Monitoring: Feed Thru Terminals		
Frequency	GND: Ground Turret		
1 GHz	Packaging		
Level	Nickel-plated machined		
+13 dBm ±2 dB into 50 ohms	aluminum housing – J2PM		
STABILITY	Mounting		
Aging (free-running)	Threaded inserts on base,		
1 x 10 <sup>-6</sup> first year	#2-56, 6 places		
	POWER REQUIREMENTS		
after 30 days operating, typical	Warm-Up Power		
5 x 10 <sup>-7</sup> second year, typical	≤ 16 Watts for 5 minutes		
3 x 10 <sup>-7</sup> per year thereafter, typical	Total Power		
Phase Noise L(f), typical	≤ 12.5 Watts at +25°C		
10 Hz -86 dBc/Hz	Supply Voltage		
100 Hz -113 dBc/Hz	+15 VDC ±5%		
1 KHz -134 dBc/Hz	ADJUSTMENT		
10 KHz -150 dBc/Hz	Loop BW		
30 KHz -151 dBc/Hz 100 KHz -153 dBc/Hz 1 MHz -172 dBc/Hz	Target Bandwidth: ~300 Hz		
100 KHz -153 dBc/Hz	Type 2 Loop		
1 MHz -172 dBc/Hz	CRYSTAL		
3 MHz -174 dBc/Hz	Туре		
10 MHz -174 dBc/Hz	100 MHz SC-cut (x10)		
Temperature Stability	OTHER ` ´		
$\pm 5 \times 10^{-7}$ free-running from 0 to	Design		
+50°C (Ref. +25°C)	Includes a SAW filter for improving the		
Harmonics	phase noise beyond the ~300 kHz offset.		
-25 dBc	Label		
Sub-Harmonics	Use conventional label with the		
-60 dBc	following information:		
PLL Divider Products	501-31288 (Current Rev.)		
-60 dBc	1 GHz GQMXO-PLM		
Spurious	+15 VDC		
-80 dBc, excluding power	Serial # - Date Code		
supply line related spurs	(Mark connectors with function)		
Phase Lock Alarm	Test Data		
TTL	- Output Level		
Locked: +3.5 VDC to +5.2 VDC (Hi)	<ul> <li>Phase Noise – free-running</li> </ul>		
Out-of-Lock: +0.8 VDC max (Lo)	<ul> <li>Temperature Stability – free-running</li> </ul>		
Phase Lock Voltage Monitor	- Harmonics, Subs, Products, Spurious		
Voltage monitor pin supplied	<ul> <li>Power – Warm-up and Total</li> </ul>		

I	REV	DATE	REVISION RECORD	DWN	AUTH
I		11-02-17	Initial Release	PAC	
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J2PM	J2PM MXO Connections		
Connector	Function		
1 2 4 5 6 7	Supply Voltage Ground, Case RF Output Phase Lock Voltage Phase Lock Alarm External Reference Input		



