INPUT
_
Frequency _
10 MHz, ±5 x 10 ⁻⁷
Level
+7 dBm ±5 dB into 50 ohms
OUTPUT
Frequency
• •
100 MHz
Level
+13 dBm ±2 dB into 50 ohms
STABILITY
Aging
1 x 10 ⁻⁶ first year
after 30 days operating, typical
5 x 10 ⁻⁷ second year, typical
3 x 10 ⁻⁷ per year thereafter, typical
Phase Noise L(f), static, free-running
100 Hz -130 dBc/Hz
1 kHz -158 dBc/Hz
10 kHz -176 dBc/Hz
100 kHz -176 dBc/Hz
Temperature Stability
$\pm 5 \times 10^{-7}$ free-running from 0 to $\pm 50^{\circ}$ C,
(Ref. +25°C)
Harmonics
-30 dBc
Sub-Harmonics
-80 dBc
PLL Divider Products
-80 dBc
Non-Harmonic Spurious
-80 dBc, excluding power
supply line related spurs
Phase Lock Alarm
TTL
Locked: +3.5 VDC to +5.2 VDC (Hi)
Out-of-Lock: +0.8 VDC max (Lo)
Phase Lock Voltage Monitor
Voltage monitor pin supplied
MECHANICAL
Dimensions
2.8" x 3.0" x 1.75"
Connectors
SMA(f) and solder pins on side
oming) and solder pins on side

Packaging

Warm-Up Power

Supply Voltage +12 VDC ±5% **ADJUSTMENT** Loop BW

Type 2 Loop

ENVIRONMENTAL Operating Temperature 0° to +50°C **Storage Temperature**

-40° to +85°C

~30 Hz, typical

following information:

100 MHz PL Citrine

Serial # - Date Code

+12 VDC

Output Level

Test Data

501-26231 (Current Rev.)

Resonance

OTHER

Label

CRYSTAL Type

Total Power

Nickel-plated machined aluminum case - CVPLOI **POWER REQUIREMENTS**

≤ 8 Watts for 5 minutes

Target Bandwidth: ≤ 10 Hz

100 MHz SC-cut (low-g) **Acceleration Sensitivity**

 \leq 3 x 10⁻¹⁰/g per axis, typical

(Internal Mount Natural Frequency)

Use conventional label with the

Phase Noise, Static, Free-Running

Power – Warm-up and Total

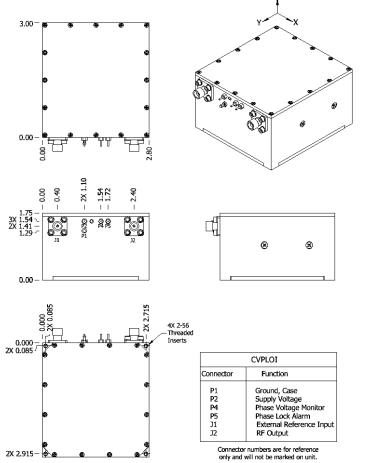
Acceleration Sensitivity

Temperature Stability, Free-Running Harmonics, Subs, Products, Spurious

Mount Natural Frequency Resonance

≤ 5 Watts at +25°C

1	REV	DATE	REVISION RECORD	DWN	AUTH		
I	-	02-15-18	Initial Release	BH	MG		
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Dimensions are in inches