| INPUT   |
|---|
| Frequency   |
| 10 MHz  |
| Level   |
| +7dBm ±6 dB into 50 ohms  |
| OUTPUT  |
| Frequency   |
| 1.6 GHz   |
| Level   |
| +13 dBm $\pm 2$ dB into 50 ohms                                 |
| STABILITY   |
| Aging (free-running)  |
| 1 x 10 <sup>-6</sup> first year                                 |
| after 30 days operating, typical                                |
| 5 x 10 <sup>-7</sup> second year, typical                       |
| 3 x 10 <sup>-7</sup> per year thereafter, typical               |
| Phase Noise L(f), typical, (free-running)                       |
|   |
| 100 Hz -104 dBc/Hz<br>1 KHz -131 dBc/Hz                         |
| 10 KHz -148 dBc/Hz  |
| 100 KHz -149 dBc/Hz   |
| Temperature Stability   |
| 1 Emperature Stability  |
| ±5 x 10 <sup>-7</sup> free-running from 0 to +50°C (Ref. +25°C) |
| (Rei. +25 C) Harmonics  |
| -25 dBc   |
| Sub-Harmonics   |
| -60 dBc   |
| PLL Divider Products  |
| -60 dBc   |
| Spurious  |
| -80 dBc, excluding power  |
| supply line related spurs                                       |
| Phase Lock Alarm  |
| TTL   |
| Locked: +3.5 VDC to +5.2 VDC (Hi)                               |
| Out-of-Lock: +0.8 VDC max (Lo)                                  |
| Phase Lock Voltage Monitor                                      |
| Voltage monitor pin supplied                                    |
| MECHANICAL  |
| Dimensions  |
| 4.4 x 4 x 1"  |

**Packaging** 

Mounting

Nickel-plated machined aluminum housing - J2P

Threaded inserts on base.

≤ 13 Watts for 5 minutes

Target Bandwidth: ≤ 10 Hz

Use conventional label with the

(Mark connectors with function)

- Phase Noise - free-running

- Power - Warm-up and Total

- Harmonics, Subs, Products, Spurious

100 MHz SC-cut (x16)

following information: 501-31541 (Current Rev.) 1.6 GHz MXO-PLD

Serial # - Date Code

- Output Level

≤ 9.5 Watts at +25°C

6 places, #2-56 **POWER REQUIREMENTS** 

Warm-Up Power

**Supply Voltage** 

**ADJUSTMENT** Loop BW

**CRYSTAL Type** 

**OTHER** Label

+15 VDC ±5%

Type 2 Loop

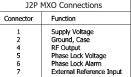
+15 VDC

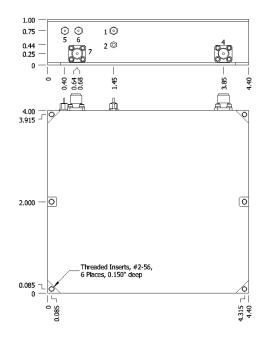
**Test Data** 

**Total Power** 

## REVISION RECORD REV DATE DWN AUTH 03-19-18 Initial Release CB **Connectors** RF Input/Output: SMA(f) Power, Monitoring: Feed Thru Terminals **GND: Ground Turret**

| J2P MXO Connections |                          |
|---------------------|--------------------------|
| Connector           | Function                 |
| 1                   | Supply Voltage           |
| 2                   | Ground, Case             |
| 4                   | RF Output                |
| 5                   | Phase Lock Voltage       |
| 6                   | Phase Lock Alarm         |
| 7                   | External Reference Input |





## Wenzel Associates, Inc. Austin, Texas 1.6 GHz Multiplied Crystal Oscillator (MXO-PLD) Date: Drawn: Ref: Rev: 501-31541 03-19-18 0.XXX Dec: Tolerances: 0.XX Dec: FSCM: (except as noted) Page 1 of 1 62821 $\pm 0.030$ " ±0.010" Dimensions are in inches